

RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

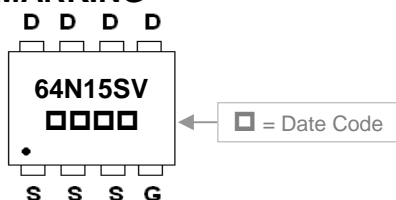
The SPR64N15SV-C is the Shielded Gate Technology N-ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The SPR64N15SV-C meet the RoHS and Green Product requirement with full function reliability approved.

FEATURES

- Shielded Gate Trench Technology
- Super Low Gate Charge
- Green Device Available

MARKING

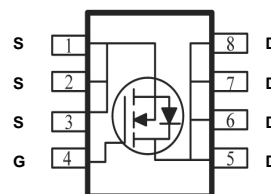


PACKAGE INFORMATION

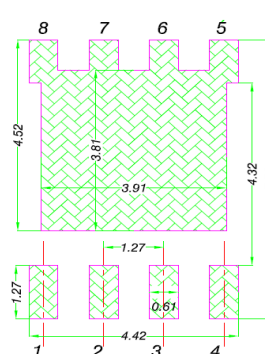
Package	MPQ	Leader Size
PR-8PP	3K	13 inch

ORDER INFORMATION

Part Number	Type
SPR64N15SV-C	Lead (Pb)-free and Halogen-free



Mounting Pad Layout



*Dimensions in millimeters

ABSOLUTE MAXIMUM RATINGS ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	150	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹ @ $V_{GS}=10\text{V}$	$T_C=25^\circ\text{C}$	64	A
	$T_C=100^\circ\text{C}$	40	
Pulsed Drain Current ²	I_{DM}	200	A
Power Dissipation	$T_C=25^\circ\text{C}$	P_D	96 W
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Thermal Resistance Ratings			
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	50	$^\circ\text{C/W}$
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	1.3	

ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	150	-	-	V	$V_{GS}=0V, I_D=250\mu A$	
Gate-Threshold Voltage	$V_{GS(th)}$	2	-	4	V	$V_{DS}=V_{GS}, I_D=250\mu A$	
Transconductance	g_{fs}	-	58	-	S	$V_{DS}=5V, I_D=20A$	
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$	
Drain-Source Leakage Current	I_{DSS}	$T_J=25^\circ\text{C}$	-	-	1	uA	$V_{DS}=120V, V_{GS}=0V$
		$T_J=55^\circ\text{C}$	-	-	100		
Static Drain-Source On-Resistance ³	$R_{DS(ON)}$	-	13	16	m Ω	$V_{GS}=10V, I_D=20A$	
Gate Resistance	R_g	-	1	-	Ω	$f=1\text{MHz}$	
Total Gate Charge	Q_g	-	29	-	nC	$I_D=20A$ $V_{DD}=75V$ $V_{GS}=10V$	
Gate-Source Charge	Q_{gs}	-	9	-			
Gate-Drain Change	Q_{gd}	-	4	-			
Turn-on Delay Time	$T_{d(on)}$	-	12	-	nS	$V_{DD}=75V$ $I_D=20A$ $V_{GS}=10V$ $R_G=10\Omega$	
Rise Time	T_r	-	8	-			
Turn-off Delay Time	$T_{d(off)}$	-	20	-			
Fall Time	T_f	-	9	-			
Input Capacitance	C_{iss}	-	2500	-	pF	$V_{GS}=0V$ $V_{DS}=75V$ $f=1\text{MHz}$	
Output Capacitance	C_{oss}	-	183	-			
Reverse Transfer Capacitance	C_{rss}	-	10	-			
Source-Drain Diode							
Diode Forward Voltage ³	V_{SD}	-	0.9	-	V	$I_F=20A, V_{GS}=0V$	
Reverse Recovery Time	t_{rr}	-	68	-	nS	$I_F=20A, V_R=75V,$	
Reverse Recovery Charge	Q_{rr}	-	116	-	nC	$di/dt=100A/\mu s$	

Notes:

1. Surface mounted on a 1 inch² FR-4 board with 2oz copper.
2. The Pulse width limited by maximum junction temperature, Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
3. The Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

CHARACTERISTIC CURVES

Fig 1. Typical Output Characteristics

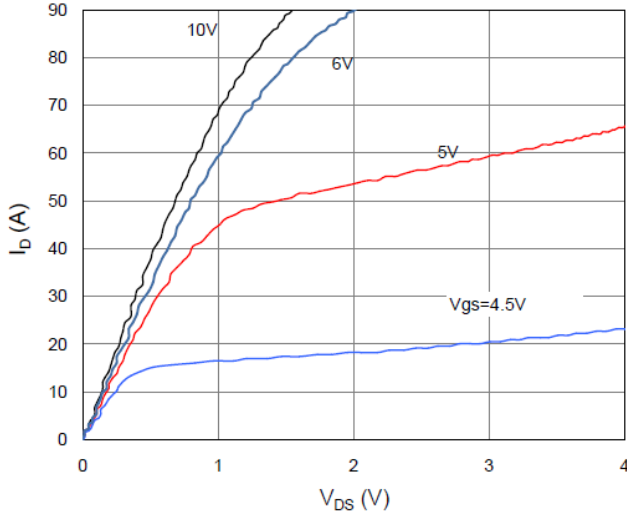


Figure 2. On-Resistance vs. Gate-Source Voltage

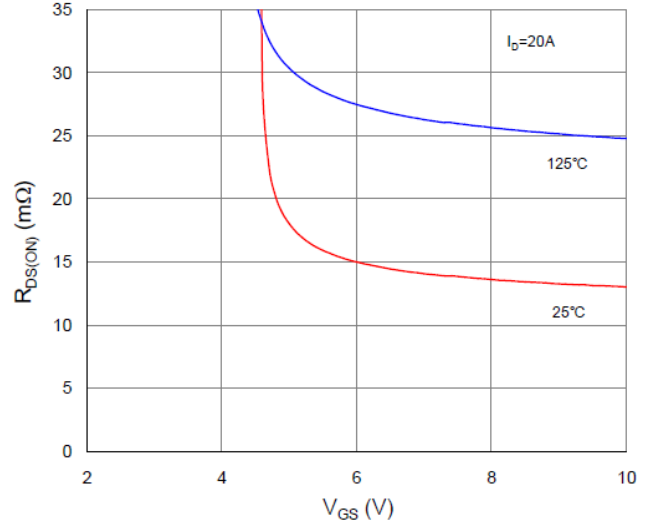


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

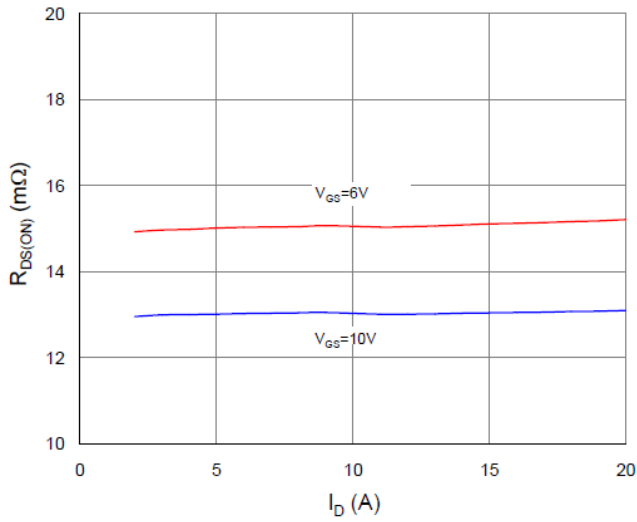


Figure 4. Normalized On-Resistance vs. Junction Temperature

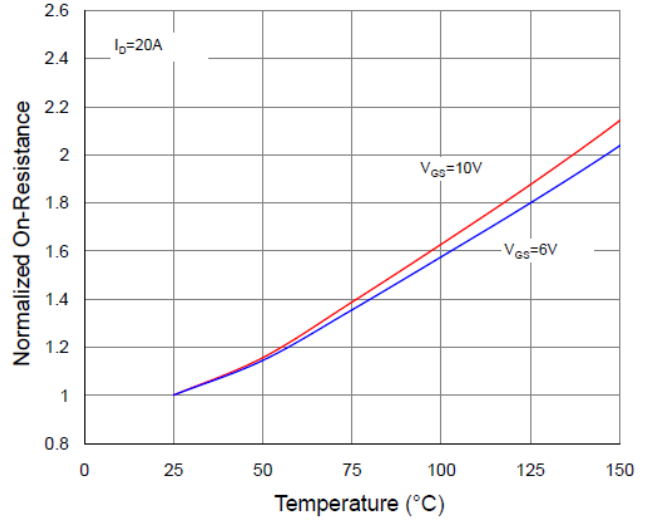


Figure 5. Typical Transfer Characteristics

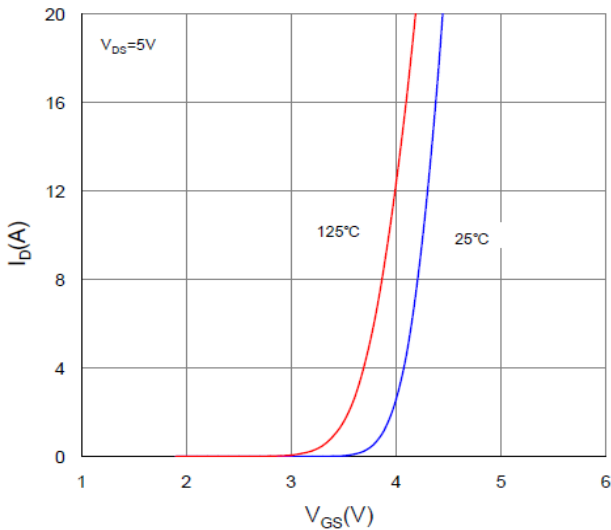
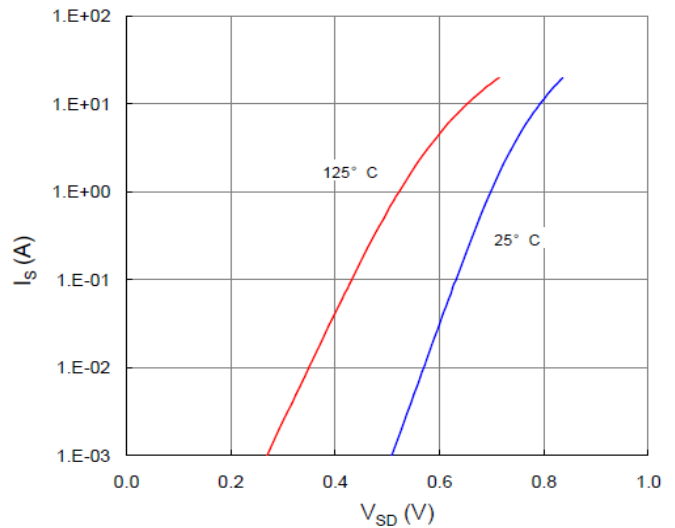


Figure 6. Typical Source-Drain Diode Forward Voltage



CHARACTERISTIC CURVES

Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage

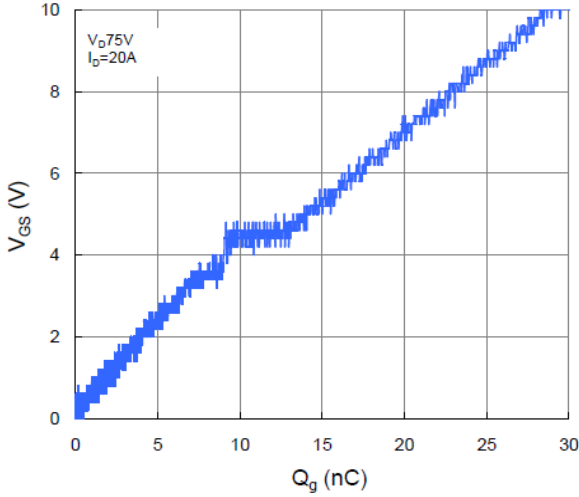


Figure 8. Typical Capacitance vs. Drain-to-Source Voltage

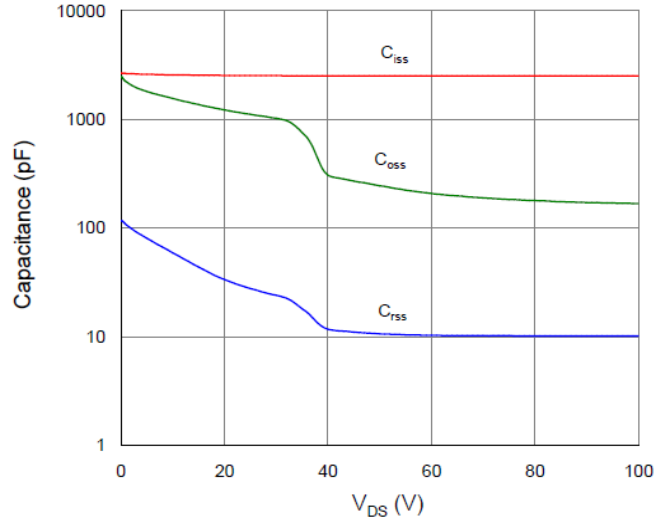


Figure 9. Maximum Safe Operating Area

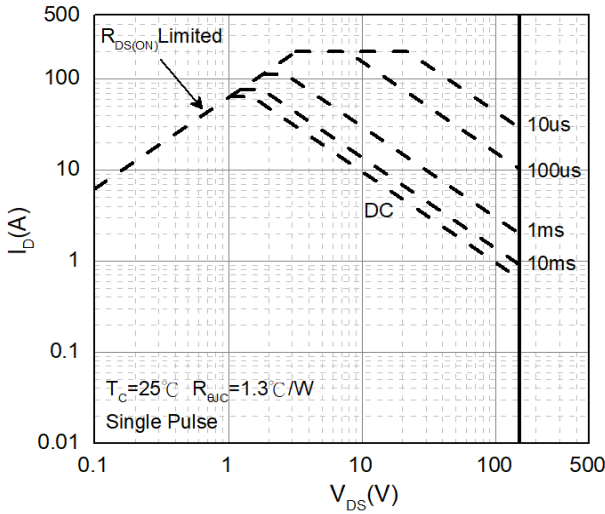


Figure 10. Maximum Drain Current vs. Case Temperature

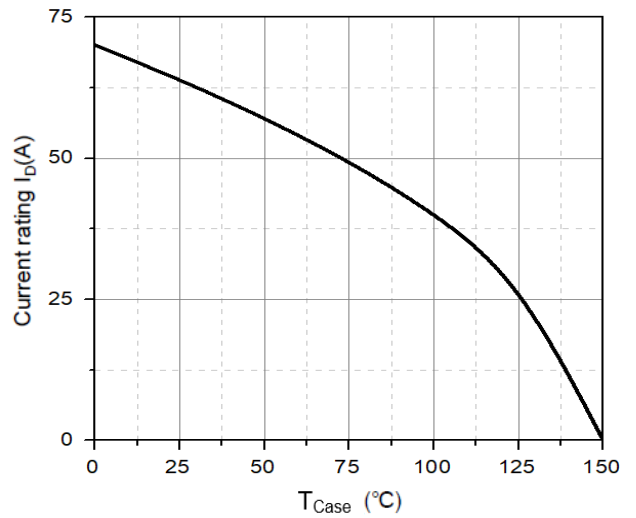


Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case

