

RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

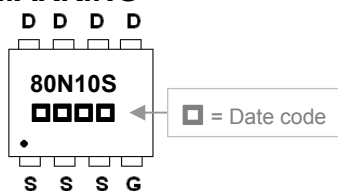
The SPR80N10S-C is the highest performance N-ch MOSFETs with extreme high cell density , which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications .

The SPR80N10S-C meet the RoHS and Green Product Requirement with full function reliability approved.

FEATURES

- High Speed Power Switching
- Green Device Available
- Super Low Gate Charge

MARKING



PACKAGE INFORMATION

Package	MPQ	Leader Size
PR-8PP	3K	13 inch

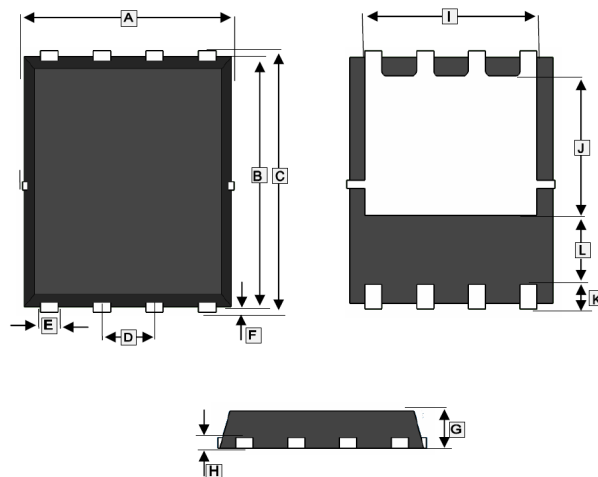
ORDER INFORMATION

Part Number	Type
SPR80N10S-C	Lead (Pb)-free and Halogen-free

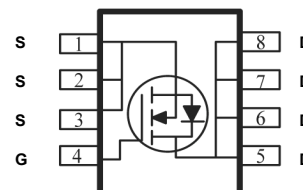
ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V_{DS}	100	V	
Gate-Source Voltage	V_{GS}	± 20	V	
Continuous Drain Current ¹ @ $V_{GS}=10\text{V}$	I_D	$T_C=25^\circ\text{C}$	80	A
		$T_C=70^\circ\text{C}$	61	A
Pulsed Drain Current ²	I_{DM}	140	A	
Total Power Dissipation ³	P_D	108	W	
Operating Junction & Storage Temperature	T_J, T_{STG}	-55~150	$^\circ\text{C}$	
Thermal Data				
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	$t \leq 10\text{sec}$	25	$^\circ\text{C} / \text{W}$
		Steady State	55	$^\circ\text{C} / \text{W}$
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	1.15	$^\circ\text{C} / \text{W}$	

PR-8PP



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.9	5.1	G	0.8	1.0
B	5.7	5.9	H	0.254	Ref.
C	5.95	6.2	I	4.0	Ref.
D	1.27 BSC.		J	3.4	Ref.
E	0.35	0.49	K	0.6	Ref.
F	0.1	0.2	L	1.4	Ref.



ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	100	-	-	V	$V_{GS}=0, I_D=250\mu\text{A}$
Gate-Threshold Voltage	$V_{GS(th)}$	1	-	2.5	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$
Forward Transfer conductance	g_{fs}	-	85	-	S	$V_{DS}=5\text{V}, I_D=20\text{A}$
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 20\text{V}$
Drain-Source Leakage Current	I_{DSS}	-	-	1	μA	$V_{DS}=80\text{V}, V_{GS}=0, T_J=25^\circ\text{C}$
		-	-	5		$V_{DS}=80\text{V}, V_{GS}=0, T_J=55^\circ\text{C}$
Static Drain-Source On-Resistance ²	$R_{DS(ON)}$	-	6.6	8.5	m Ω	$V_{GS}=10\text{V}, I_D=13.5\text{A}$
		-	8.7	10.5		$V_{GS}=4.5\text{V}, I_D=11.5\text{A}$
Total Gate Charge	Q_g	-	45	-	nC	$I_D=13.5\text{A}$ $V_{DS}=50\text{V}$ $V_{GS}=10\text{V}$
Gate-Source Charge	Q_{gs}	-	9.5	-		
Gate-Drain ("Miller") Charge	Q_{gd}	-	4.8	-		
Turn-on Delay Time	$T_{d(on)}$	-	10	-	nS	$V_{DD}=50\text{V}$ $I_D=13.5\text{A}$ $V_{GS}=10\text{V}$ $R_G=3\Omega$
Rise Time	T_r	-	6.5	-		
Turn-off Delay Time	$T_{d(off)}$	-	45	-		
Fall Time	T_f	-	7.5	-		
Input Capacitance	C_{iss}	-	3320	-	pF	$V_{GS}=0$ $V_{DS}=50\text{V}$ $f=1.0\text{MHz}$
Output Capacitance	C_{oss}	-	605	-		
Reverse Transfer Capacitance	C_{rss}	-	20	-		
Source-Drain Diode						
Diode Forward Voltage ²	V_{SD}	-	-	1.2	V	$I_S=1\text{A}, V_{GS}=0\text{V}$
Continuous Source Current ¹	I_S	-	-	80	A	$V_G=V_D=0$, Force Current
Reverse Recovery Time	t_{rr}	-	33	-	nS	$I_F=13.5\text{A}, dI/dt=100\text{A}/\mu\text{s},$ $T_J=25^\circ\text{C}$
Reverse Recovery Charge	Q_{rr}	-	150	-	nC	

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. The power dissipation is limited by 150°C junction temperature.

CHARACTERISTIC CURVES

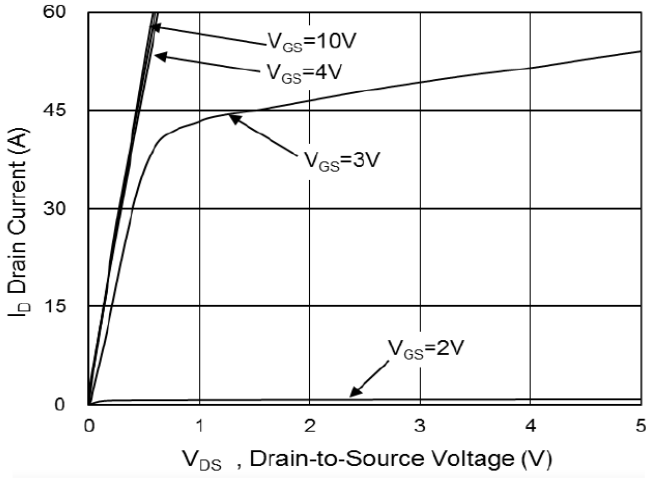


Fig.1 Typical Output Characteristics

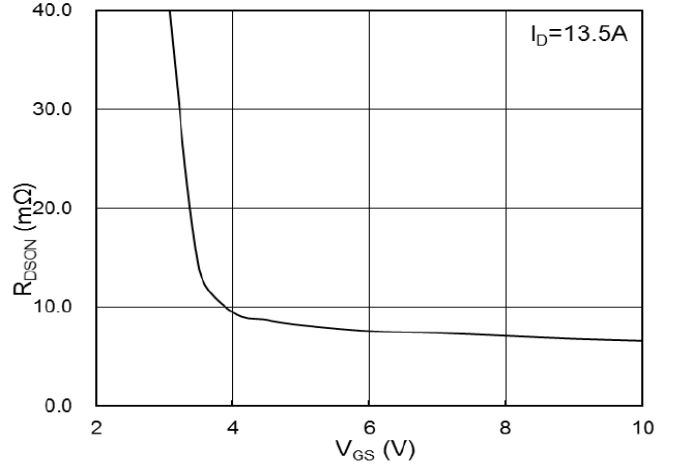


Fig.2 On-Resistance vs G-S Voltage

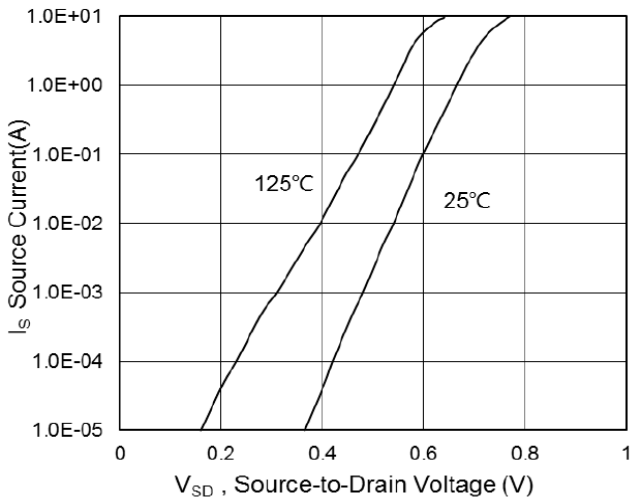


Fig.3 Source-Drain Forward Characteristics

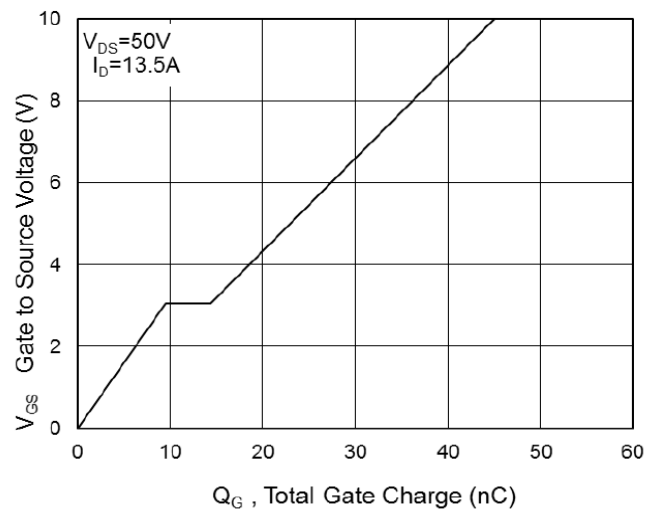


Fig.4 Gate-Charge Characteristics

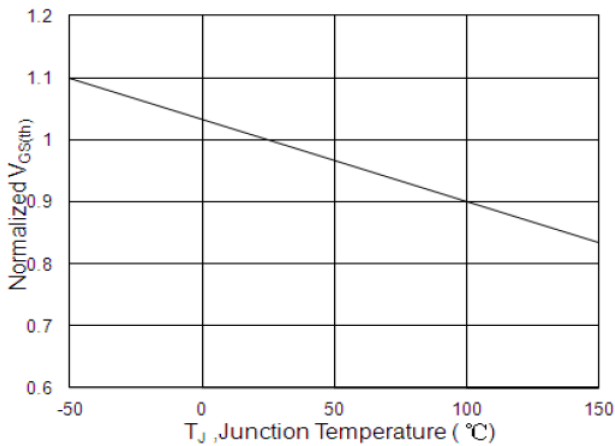


Fig.5 Normalized $V_{GS(th)}$ vs T_J

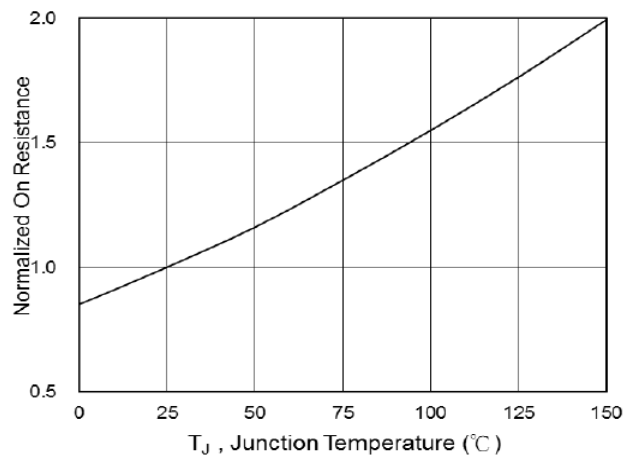


Fig.6 Normalized $R_{DS(ON)}$ vs T_J

CHARACTERISTIC CURVES

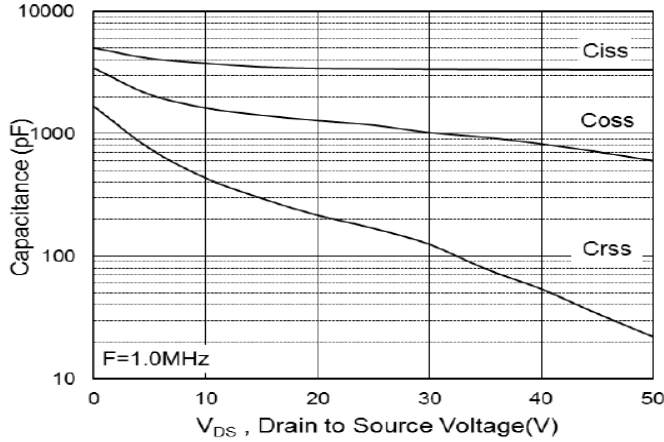


Fig.7 Capacitance

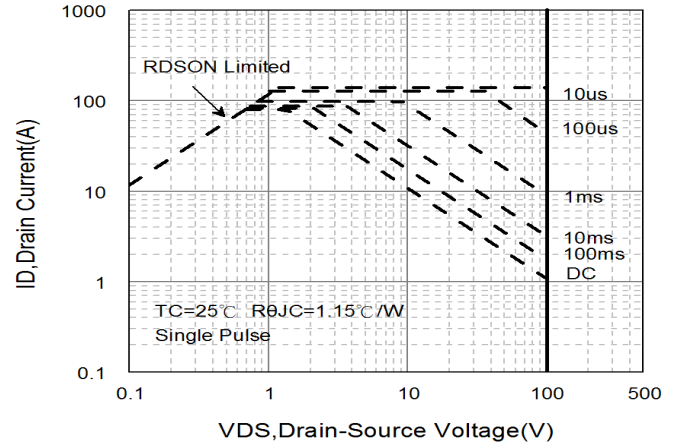


Fig.8 Safe Operating Area

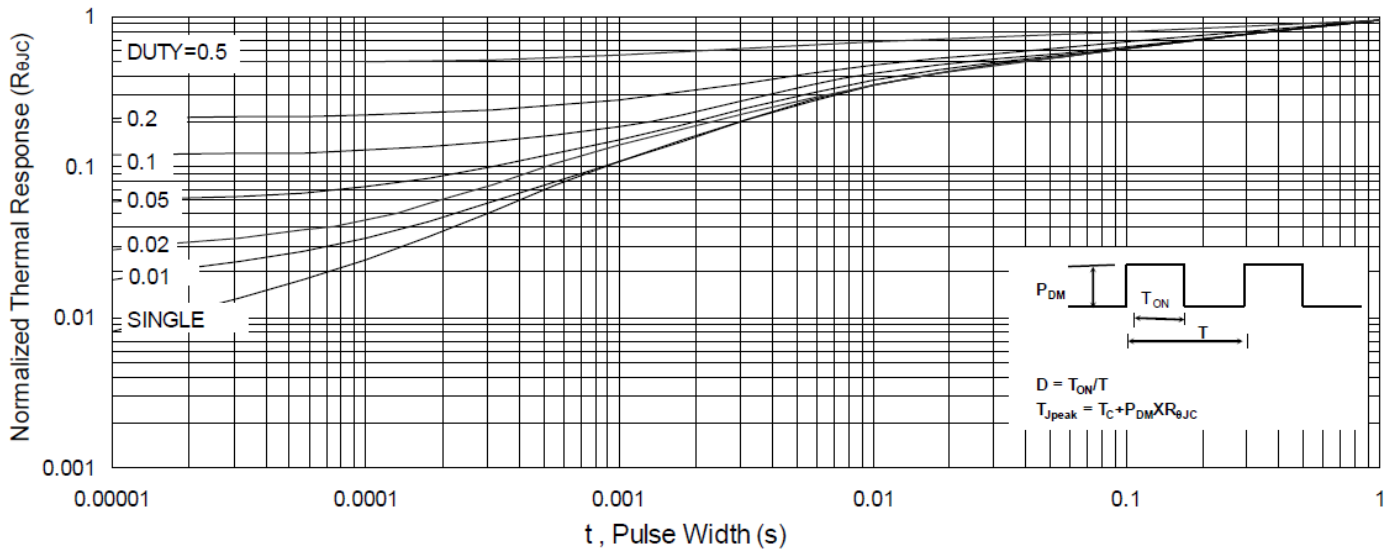


Fig.9 Normalized Maximum Transient Thermal Impedance

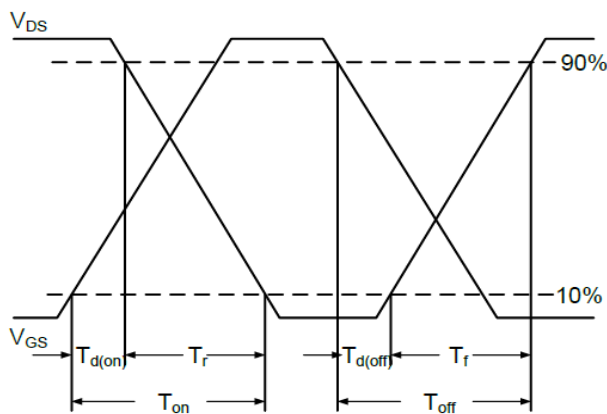


Fig.10 Switching Time Waveform

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

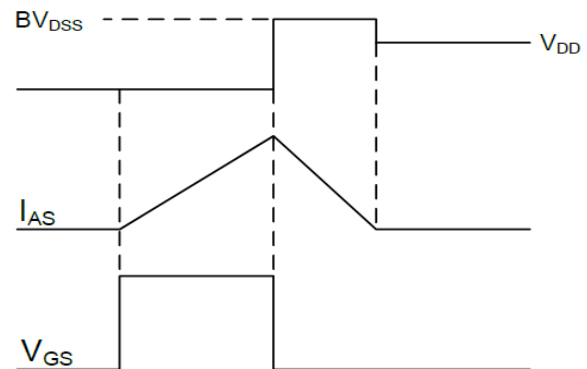


Fig.11 Unclamped Inductive Switching Waveform