

RoHS Compliant Product
 A suffix of "-C" specifies halogen and lead-free

FEATURES

- 150V/1.25A
 $R_{DS(ON)} \leq 320m\Omega$ @ $V_{GS}=10V$
 $R_{DS(ON)} \leq 350m\Omega$ @ $V_{GS}=6V$
- Reliable and Rugged
- Green Device Available

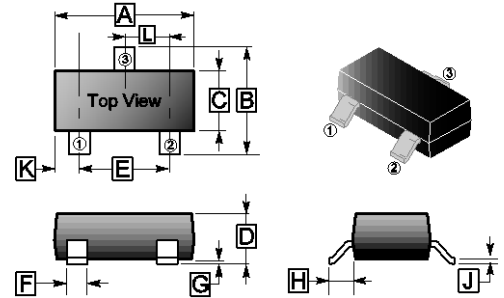
APPLICATION

Power Management in Notebook Computer,
 Portable Equipment and Battery Powered Systems.

MARKING

3215

SC-59



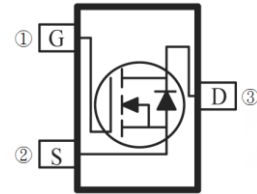
REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.70	3.10	G	0.10	REF.
B	2.10	3.00	H	0.40	REF.
C	1.20	1.70	J	0.047	0.207
D	0.89	1.40	K	0.50	REF.
E	2.00	TYP.	L	0.95	REF.
F	0.30	0.50			

PACKAGE INFORMATION

Package	MPQ	Leader Size
SC-59	3K	7 inch

ORDER INFORMATION

Part Number	Type
SMG3215-C	Lead (Pb)-free and Halogen-free



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	150	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹ @ $V_{GS}=10V$	$T_A=25^\circ C$	1.25	A
	$T_A=70^\circ C$	1	
Pulsed Drain Current ³	I_{DM}	5	A
Total Power Dissipation	$T_A=25^\circ C$	1	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ C$
Thermal Resistance Ratings			
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	125	$^\circ C/W$
Thermal Resistance Junction-Ambient ²		270	
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	80	

ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

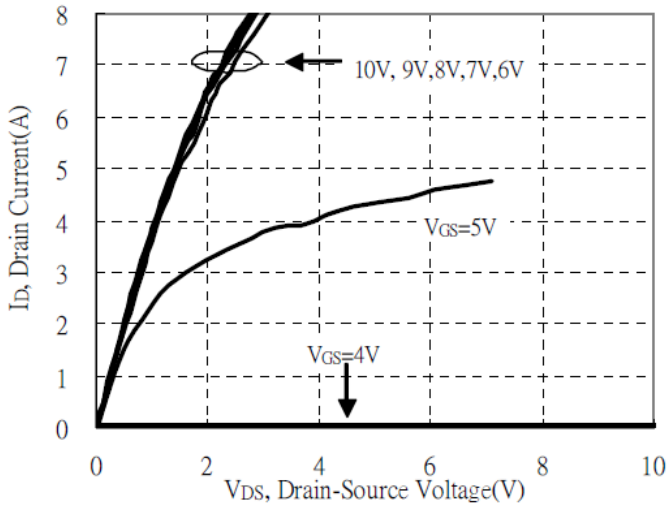
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
Drain-Source Breakdown Voltage	BV_{DSS}	150	-	-	V	$V_{GS}=0, I_D=250\mu\text{A}$	
Gate Threshold Voltage	$V_{GS(th)}$	2	-	4	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	
Forward Transconductance	g_{fs}	-	2.5	-	S	$V_{DS}=15\text{V}, I_D=1\text{A}$	
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 20\text{V}$	
Drain-Source Leakage Current	I_{DSS}	$T_J=25^\circ\text{C}$	-	-	1	μA	$V_{DS}=120\text{V}, V_{GS}=0$
		$T_J=55^\circ\text{C}$	-	-	10		
Static Drain-Source On-Resistance ⁴	$R_{DS(ON)}$	-	260	320	m Ω	$V_{GS}=10\text{V}, I_D=1\text{A}$	
		-	280	350		$V_{GS}=6\text{V}, I_D=1\text{A}$	
Total Gate Charge	Q_g	-	7.5	-	nC	$I_D=1\text{A}$ $V_{DS}=75\text{V}$ $V_{GS}=10\text{V}$	
Gate-Source Charge	Q_{gs}	-	1.5	-			
Gate-Drain Charge	Q_{gd}	-	2	-			
Turn-on Delay Time	$T_{d(on)}$	-	12	-	nS	$V_{DS}=75\text{V}$ $I_D=1\text{A}$ $V_{GS}=10\text{V}$ $R_G=6\Omega$	
Rise Time	T_r	-	16	-			
Turn-off Delay Time	$T_{d(off)}$	-	32	-			
Fall Time	T_f	-	17	-			
Input Capacitance	C_{iss}	-	290	-	pF	$V_{GS}=0$ $V_{DS}=30\text{V}$ $f=1\text{MHz}$	
Output Capacitance	C_{oss}	-	30	-			
Reverse Transfer Capacitance	C_{rss}	-	12	-			
Source-Drain Diode							
Continuous Source Current ¹	I_S	-	-	1.25	A		
Pulsed Source Current ³	I_{SM}	-	-	5			
Forward on Voltage ⁴	V_{SD}	-	-	1.2	V	$I_S=1\text{A}, V_{GS}=0$	
Reverse Recovery Time	t_{rr}	-	44.5	-	nS	$I_F=1.25\text{A}, di/dt=100\text{A}/\mu\text{s}$	
Reverse Recovery Charge	Q_{rr}	-	15.8	-	nC	$T_J=25^\circ\text{C}$	

Notes:

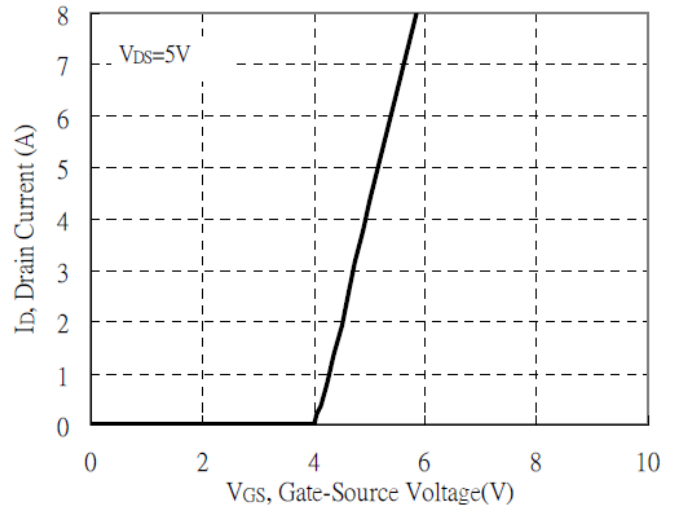
- Surface mounted on 1"x1" FR-4 board with 2oz copper.
- When mounted on Min. copper pad.
- Pulse width limited by maximum junction temperature, Pulse Width $\leq 100\mu\text{s}$, Duty Cycle $\leq 1\%$.
- Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

CHARACTERISTIC CURVES

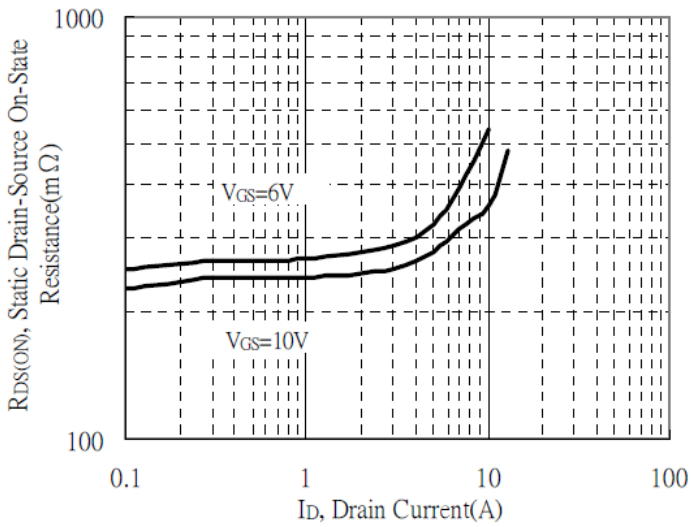
Typical Output Characteristics



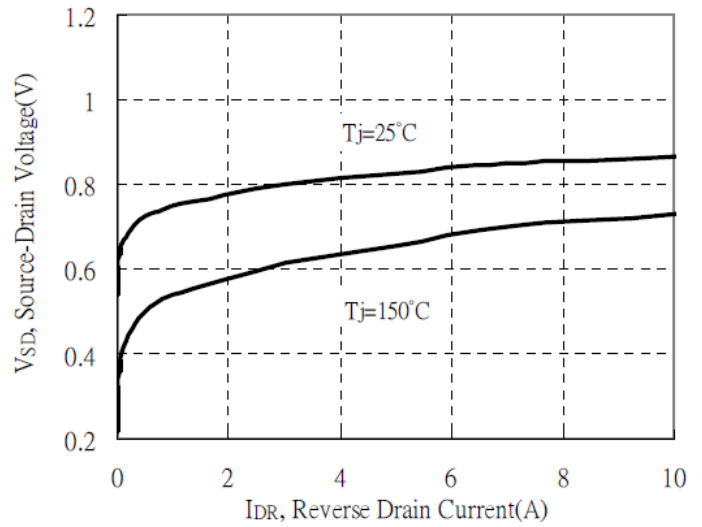
Typical Transfer Characteristics



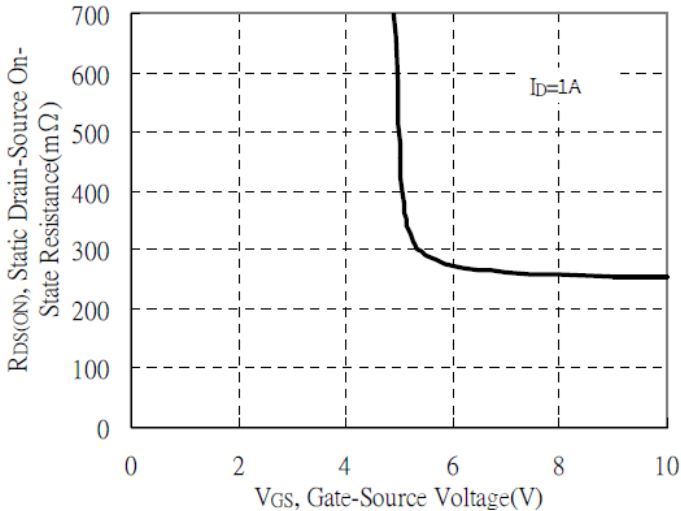
Static Drain-Source On-State resistance vs Drain Current



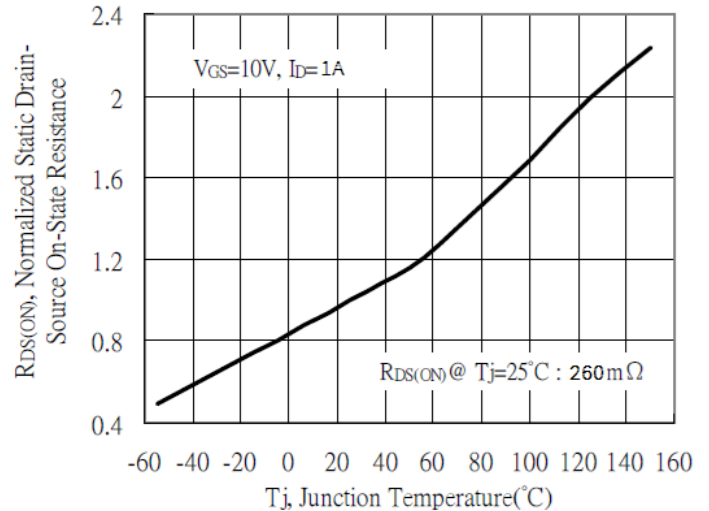
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

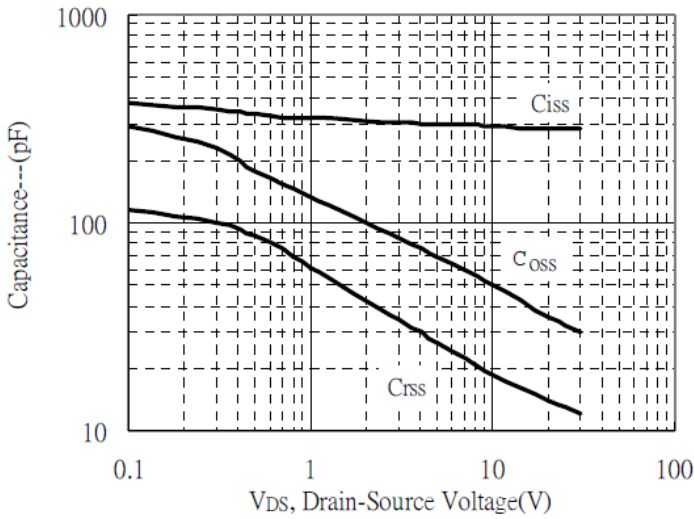


Drain-Source On-State Resistance vs Junction Temperature

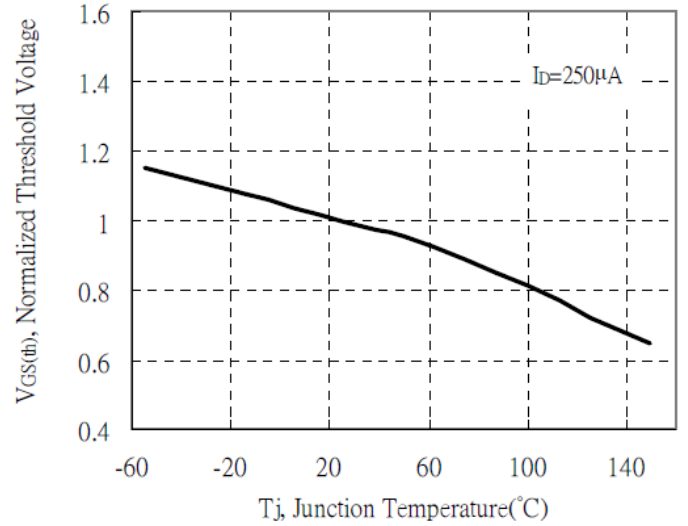


CHARACTERISTIC CURVES

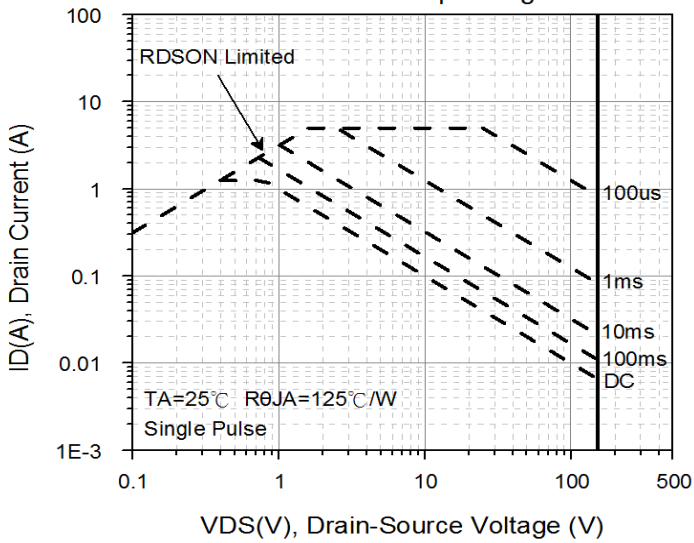
Capacitance vs Drain-to-Source Voltage



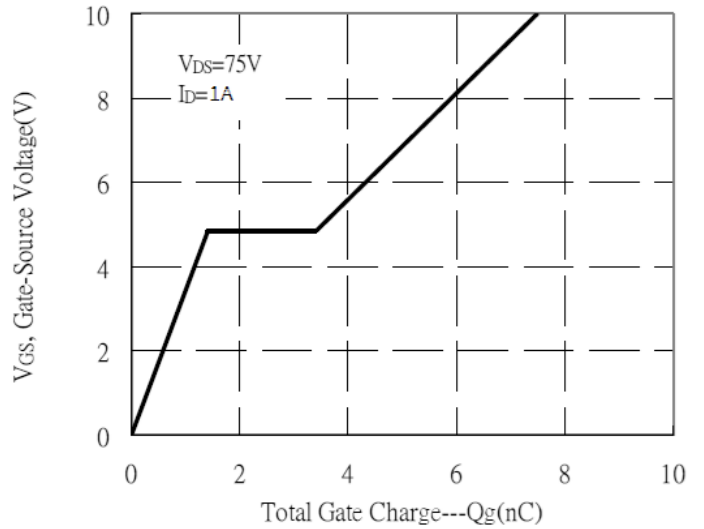
Threshold Voltage vs Junction Temperature



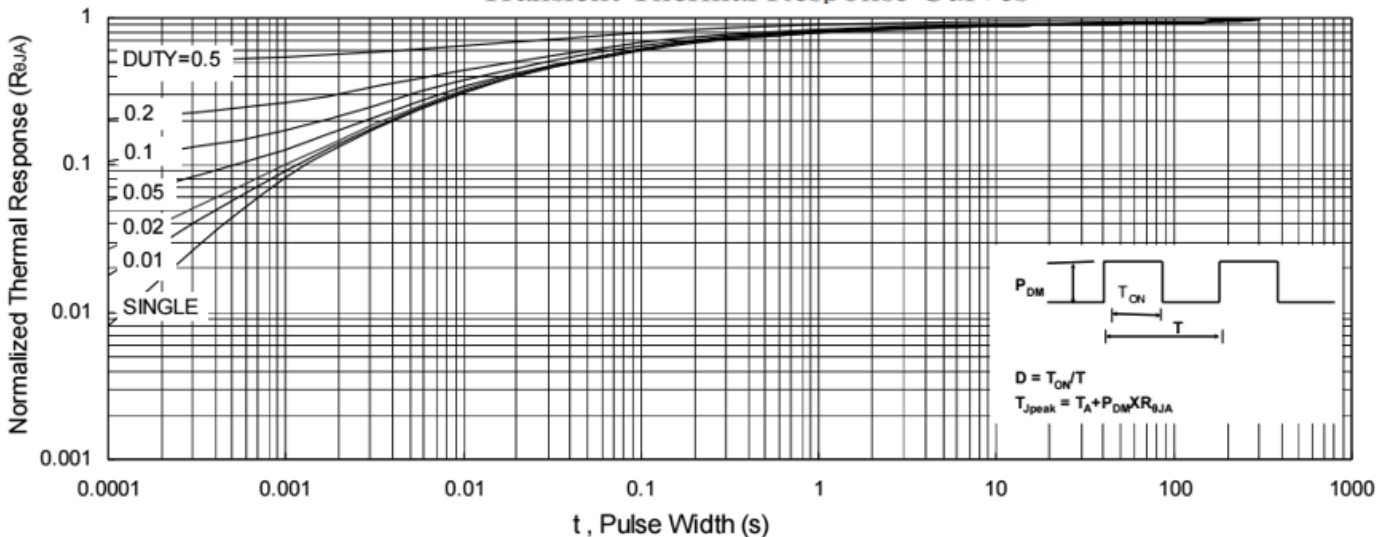
Maximum Safe Operating Area



Gate Charge Characteristics



Transient Thermal Response Curves



CHARACTERISTIC CURVES

