Elektronische Bauelemente

SSG13N10SV-C

13A, 100V, $R_{DS(ON)}$ 9mΩ

N-Channel Shielded Gate Trench Power MOSFET

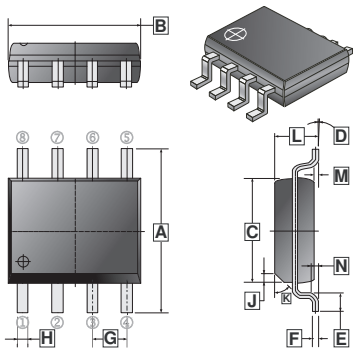
RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

The SSG13N10SV-C is the Shielded Gate Technology N-ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The SSG13N10SV-C meet the RoHS and Green Product requirement with full function reliability approved.

SOP-8



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	5.79	6.20	H	0.33	0.51
B	4.70	5.11	J	0.375 REF.	
C	3.80	4.00	K	45°REF.	
D	0°	8°	L	1.3	1.752
E	0.40	1.27	M	0	0.25
F	0.10	0.25	N	0.25 REF.	
G	1.27 TYP.				

FEATURES

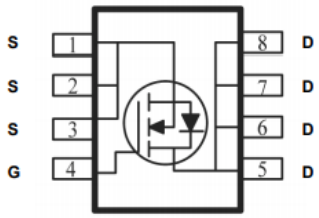
- Shielded Gate Trench Technology
- Super Low Gate Charge
- Green Device Available

PACKAGE INFORMATION

Package	MPQ	Leader Size
SOP-8	2.5K	13 inch

ORDER INFORMATION

Part Number	Type
SSG13N10SV-C	Lead (Pb)-free and Halogen-free



ABSOLUTE MAXIMUM RATINGS (T_A=25℃ unless otherwise specified)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current [1] @V_{GS}=10V	T_A=25℃	I_D	13	A
	T_A=70℃		10	
Pulsed Drain Current [2]		I_{DM}	55	A
Power Dissipation [3]	T_A=25℃	P_D	3.1	W
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55~150	℃
Thermal Data				
Thermal Resistance Junction-Ambient [1]		$R_{\theta JA}$	t≦10s, 40	℃/W
			Steady State, 75	
Thermal Resistance Junction-Case [1]		$R_{\theta JC}$	24	

ELECTRICAL CHARACTERISTICS (T_J=25℃ unless otherwise specified)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-Source Breakdown Voltage		BV_{DSS}	100	-	-	V	V_{GS}=0V, I_D=250μA
Gate-Threshold Voltage		$V_{GS(th)}$	2	-	4	V	V_{DS}=V_{GS}, I_D=250μA
Forward Transfer Conductance		g_{fs}	-	80	-	S	V_{DS}=5V, I_D=20A
Gate-Source Leakage Current		I_{GSS}	-	-	±100	nA	V_{GS}=±20V
Drain-Source Leakage Current	T_J=25℃	I_{DSS}	-	-	1	μA	V_{DS}=80V, V_{GS}=0
	T_J=55℃		-	-	5		V_{DS}=80V, V_{GS}=0
Drain-Source On-Resistance [2]		$R_{DS(ON)}$	-	6.6	9	mΩ	V_{GS}=10V, I_D=13A
Total Gate Charge		Q_g	-	52	-	nC	I_D=13A V_{DS}=50V V_{GS}=10V
Gate-Source Charge		Q_{gs}	-	13.5	-		
Gate-Drain Charge		Q_{gd}	-	16	-		
Turn-On Delay Time		$T_{d(on)}$	-	10	-	nS	V_{DD}=50V I_D=13A V_{GS}=10V R_G=3Ω
Rise Time		T_r	-	6.5	-		
Turn-Off Delay Time		$T_{d(off)}$	-	45	-		
Fall Time		T_f	-	7.5	-		
Input Capacitance		C_{iss}	-	3148	-	pF	V_{GS}=0V V_{DS}=50V f=1MHz
Output Capacitance		C_{oss}	-	693	-		
Reverse Transfer Capacitance		C_{rss}	-	26	-		
Source-Drain Diode							
Continuous Source Current [1]		I_S	-	-	13	A	V_G=V_D=0, Force Current
Pulsed Source Current [2]		I_{SM}	-	-	55	A	
Forward On Voltage [2]		V_{SD}	-	-	1.2	V	I_S=1A, V_{GS}=0V
Reverse Recovery Time		T_{rr}	-	33	-	nS	I_S=13A, dI/dt=100A/μs, T_J=25℃
Reverse Recovery Charge		Q_{rr}	-	150	-	nC	

Notes:
1. Surface mounted on a 1inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width≦300us, duty cycle≦2%.
3. The power dissipation is limited by 150℃ juncti on temperature.

CHARACTERISTICS CURVE

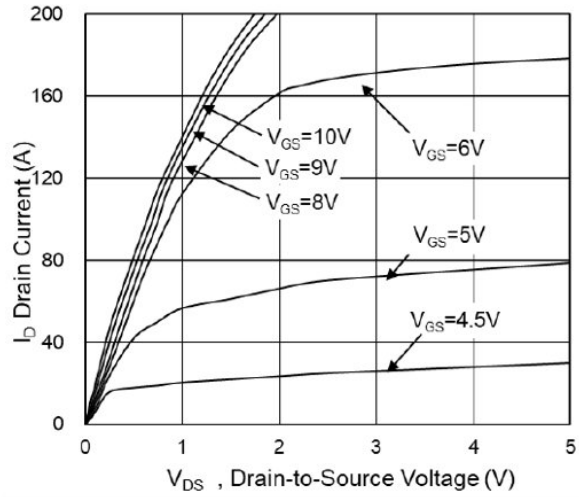


Fig.1 Typical Output Characteristics

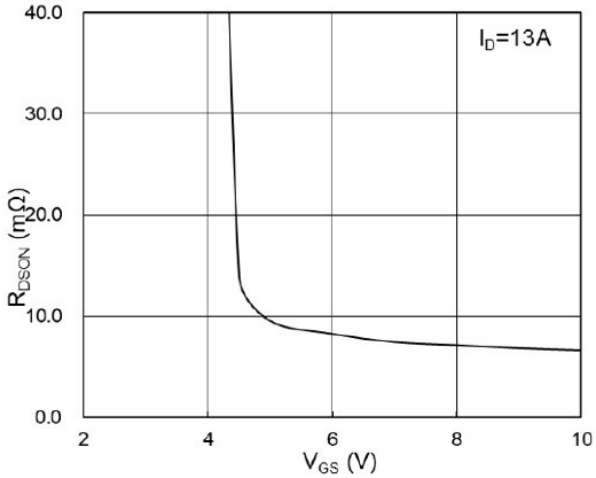


Fig.2 On-Resistance vs G-S Voltage

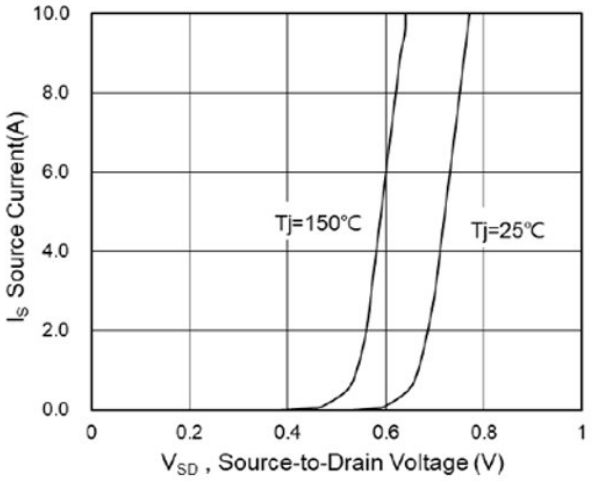


Fig.3 Source-Drain Forward Characteristics

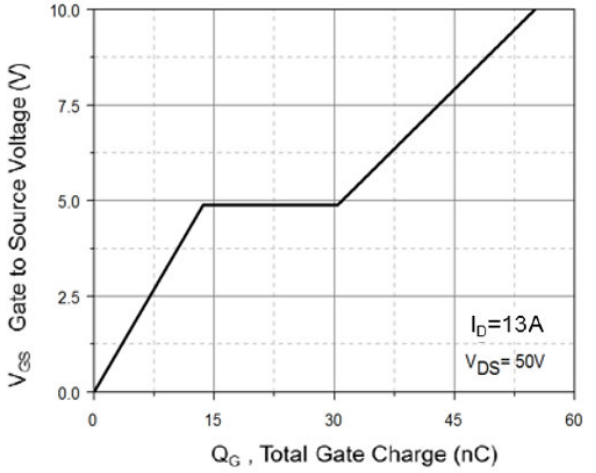


Fig.4 Gate-Charge Characteristics

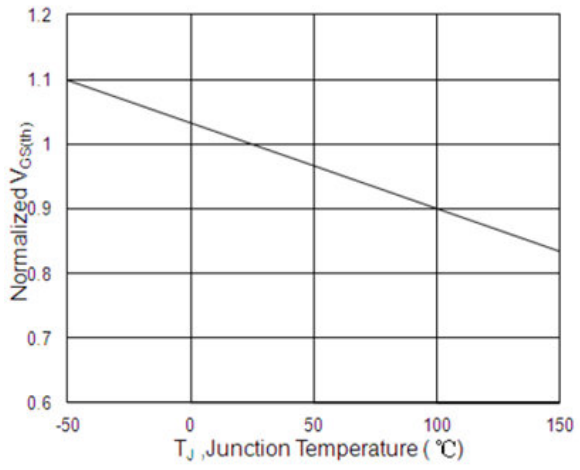


Fig.5 Normalized $V_{GS(th)}$ vs T_J

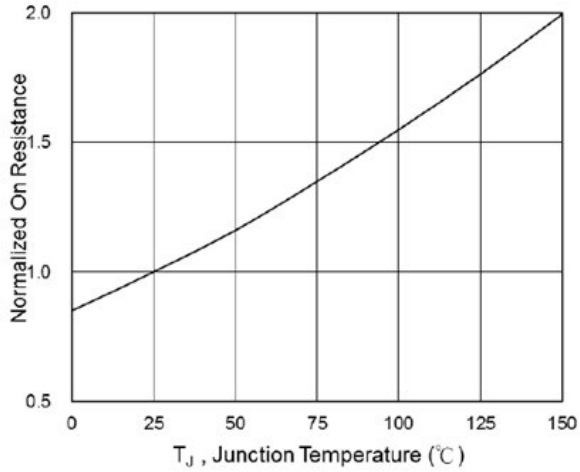


Fig.6 Normalized R_{DSON} vs T_J

CHARACTERISTICS CURVE

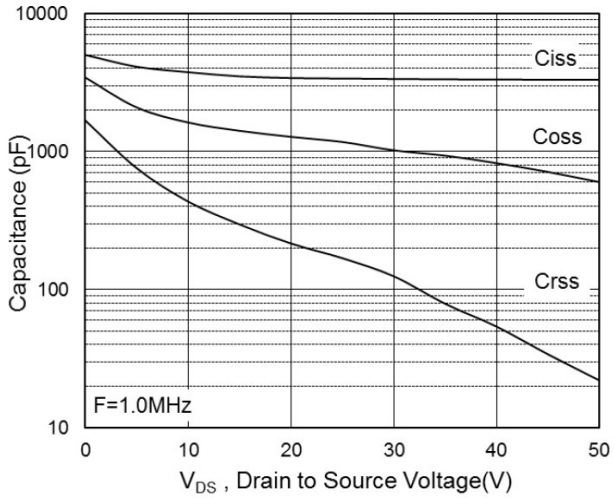


Fig.7 Capacitance

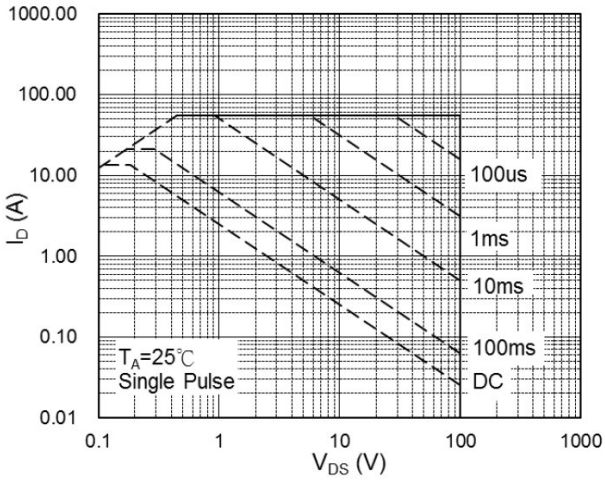


Fig.8 Safe Operating Area

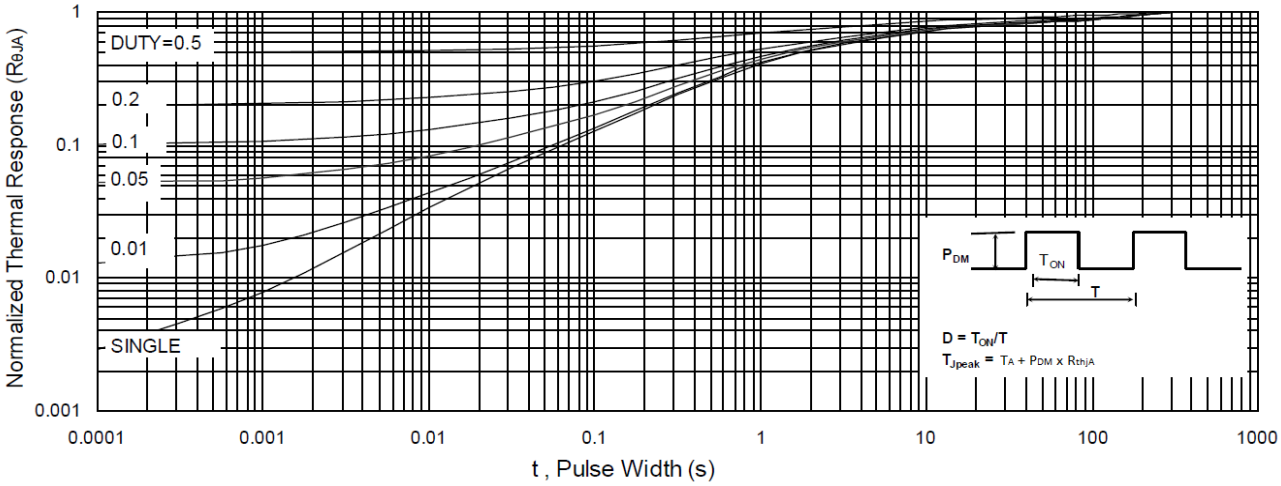


Fig.9 Normalized Maximum Transient Thermal Impedance

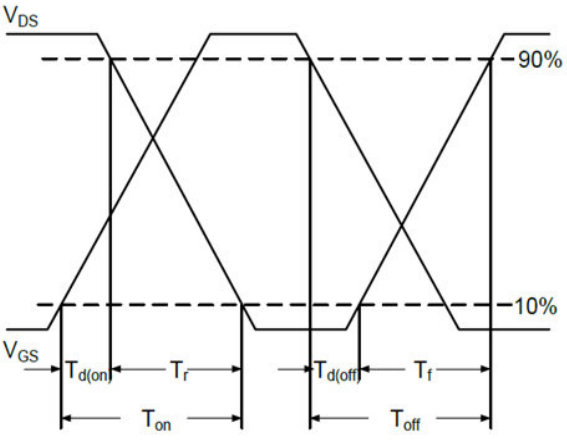


Fig.10 Switching Time Waveform

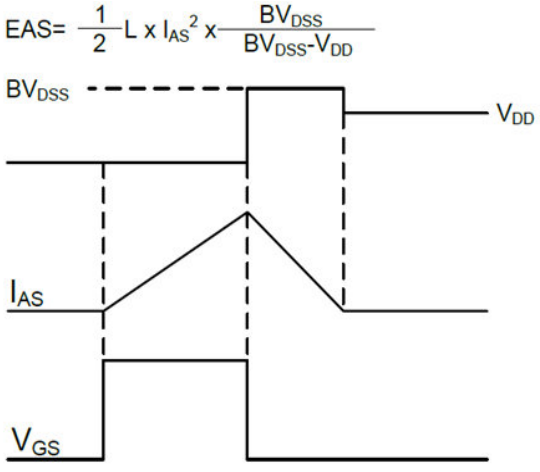


Fig.11 Unclamped Inductive Switching Waveform