Elektronische Bauelemente

SSG3624-C

9A, 30V, $R_{DS(ON)}$ 12mΩ
Dual N-Ch Fast Switching MOSFET

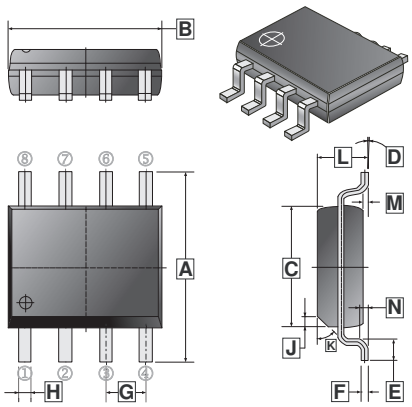
RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

The SSG3624-C is the high cell density trenched dual N-Ch MOSFETs, which provides excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The SSG3624-C meets the RoHS and Green Product requirement with full function reliability approved.

SOP-8

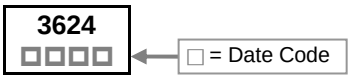


REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	5.79	6.20	H	0.33	0.51
B	4.70	5.11	J	0.375 REF.	
C	3.80	4.00	K	45° REF.	
D	0°	8°	L	1.30	1.752
E	0.40	1.27	M	0	0.25
F	0.10	0.25	N	0.25 REF.	
G	1.27 TYP.				

FEATURES

- Super Low Gate Charge
- High Speed Power Switching
- Green Device Available
- Advanced High Cell Density Trench Technology

MARKING

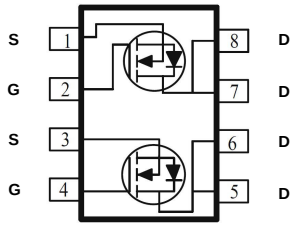


PACKAGE INFORMATION

Package	MPQ	Leader Size
SOP-8	2.5K	13 inch

ORDER INFORMATION

Part Number	Type
SSG3624-C	Lead (Pb)-free and Halogen-free



ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Ratings	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current [1] @V_{GS}=10V	T_A=25℃	I_D	9	A
	T_A=70℃		7	
Pulsed Drain Current [2]		I_{DM}	36	A
Total Power Dissipation [3]	T_A=25℃	P_D	1.5	W
Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	℃
Thermal Resistance Ratings				
Thermal Resistance Junction-Ambient [1]		$R_{\theta JA}$	85	℃/W
Thermal Resistance Junction-Case [1]		$R_{\theta JC}$	25	

ELECTRICAL CHARACTERISTICS (T_J=25℃ unless otherwise specified)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage		BV_{DSS}	30	-	-	V	V_{GS}=0V, I_D=250μA
Gate Threshold Voltage		$V_{GS(th)}$	1.2	-	2.5	V	V_{DS}=V_{GS}, I_D=250μA
Forward Transfer Conductance		g_{fs}	-	24	-	S	V_{DS}=5V, I_D=8A
Gate-Source Leakage Current		I_{GSS}	-	-	±100	nA	V_{GS}=±20V, V_{DS}=0V
Drain-Source Leakage Current	T_J=25℃	I_{DSS}	-	-	1	uA	V_{DS}=24V, V_{GS}=0V
	T_J=55℃		-	-	5		V_{DS}=24V, V_{GS}=0V
Static Drain-Source On-Resistance [2]		$R_{DS(ON)}$	-	-	12	mΩ	V_{GS}=10V, I_D=8A
			-	-	18		V_{GS}=4.5V, I_D=6A
Gate Resistance		R_g	-	1.8	-	Ω	V_{DS}=V_{GS}=0V, f=1MHz
Total Gate Charge		Q_g	-	9.63	-	nC	V_{DS}=15V V_{GS}=4.5V I_D=8A
Gate-Source Charge		Q_{gs}	-	3.88	-		
Gate-Drain Charge		Q_{gd}	-	3.44	-		
Turn-on Delay Time		$T_{d(on)}$	-	4.2	-	nS	V_{DD}=15V V_{GS}=10V I_D=8A R_G=1.5Ω
Rise Time		T_r	-	8.2	-		
Turn-off Delay Time		$T_{d(off)}$	-	31	-		
Fall Time		T_f	-	4	-		
Input Capacitance		C_{iss}	-	940	-	pF	V_{GS}=0V V_{DS}=15V f=1MHz
Output Capacitance		C_{oss}	-	131	-		
Reverse Transfer Capacitance		C_{rss}	-	109	-		
Source-Drain Diode							
Continuous Source Current [1]		I_S	-	-	9	A	V_G=V_D=0V, Force Current
Diode Forward Voltage [2]		V_{SD}	-	-	1	V	V_{GS}=0V, I_S=1A, T_J=25℃
Reverse Recovery Time		t_{rr}	-	8	-	nS	I_F=8A, dI/dt=100A/μs, T_J=25℃
Reverse Recovery Charge		Q_{rr}	-	2.9	-	nC	

Notes:
1. The data tested by surface mounted on 1inch^2 FR4 Board with 2OZ copper.
2. The data tested by pulsed, Pulse Width≦300μs, Duty Cycle≦2%.
3. The power dissipation is limited by 150℃ juncti on temperature.

CHARACTERISTICS CURVE

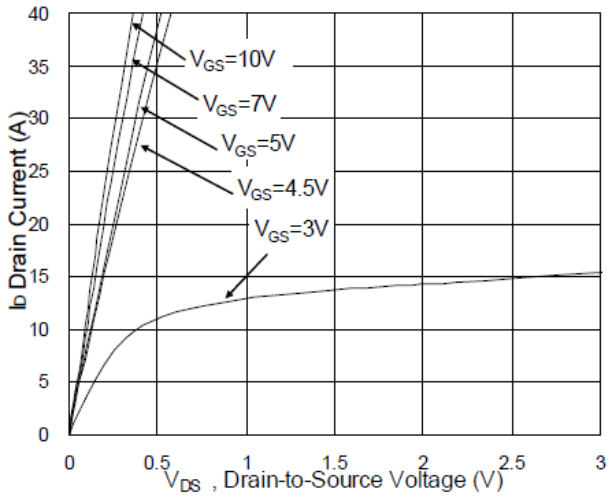


Fig.1 Typical Output Characteristics

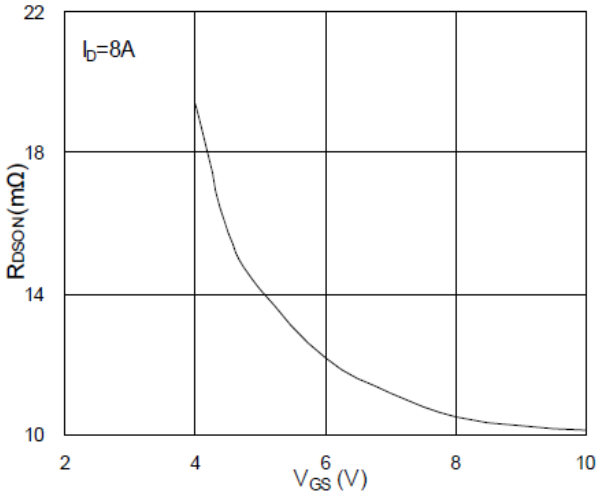


Fig.2 On-Resistance vs. G-S Voltage

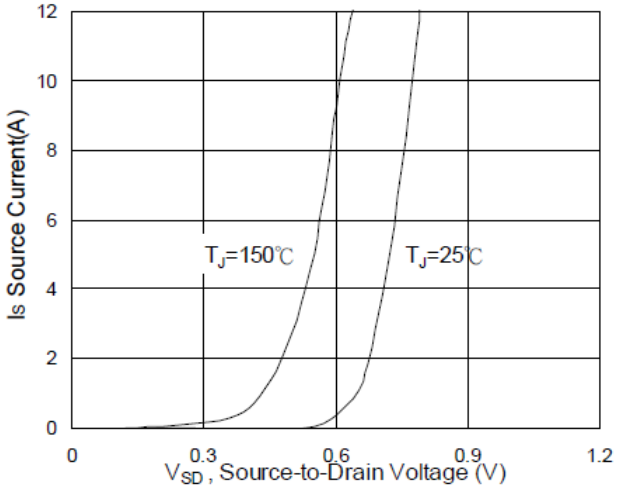


Fig.3 Source Drain Forward Characteristics

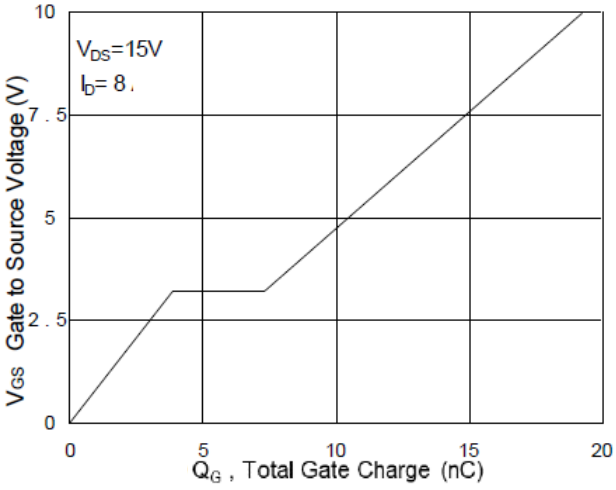


Fig.4 Gate-Charge Characteristics

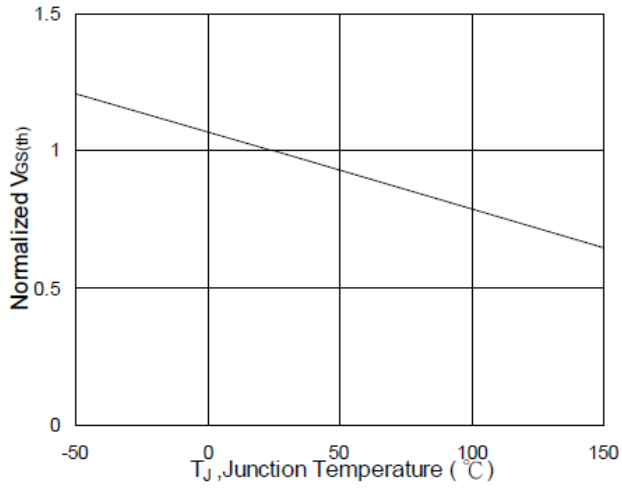


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

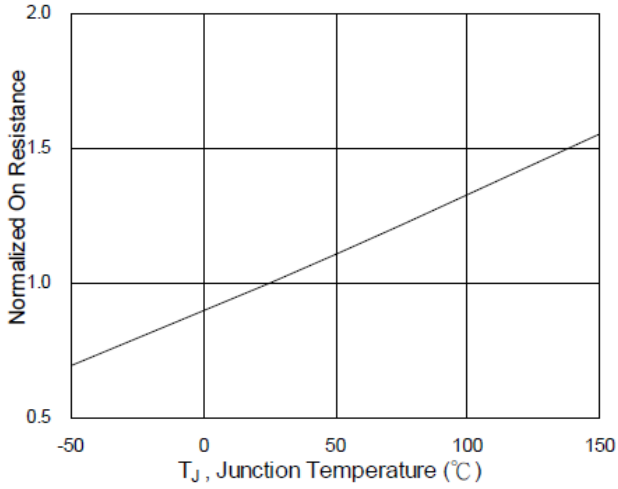


Fig.6 Normalized R_{DSON} vs. T_J

CHARACTERISTICS CURVE

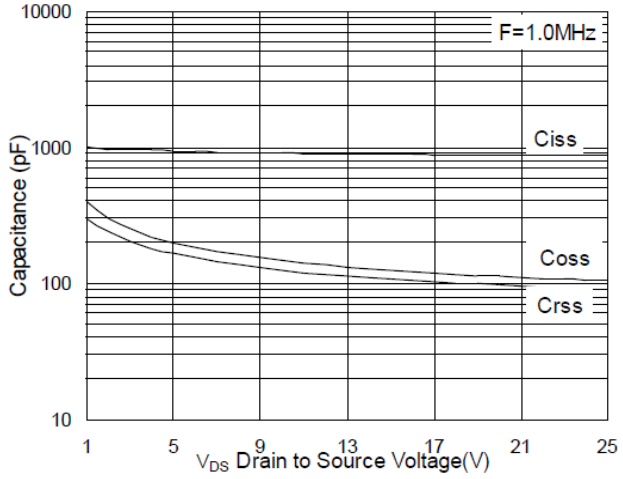


Fig.7 Capacitance

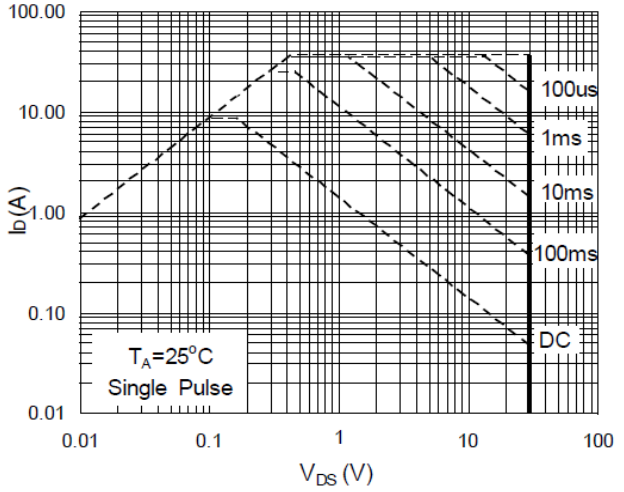


Fig.8 Safe Operating Area

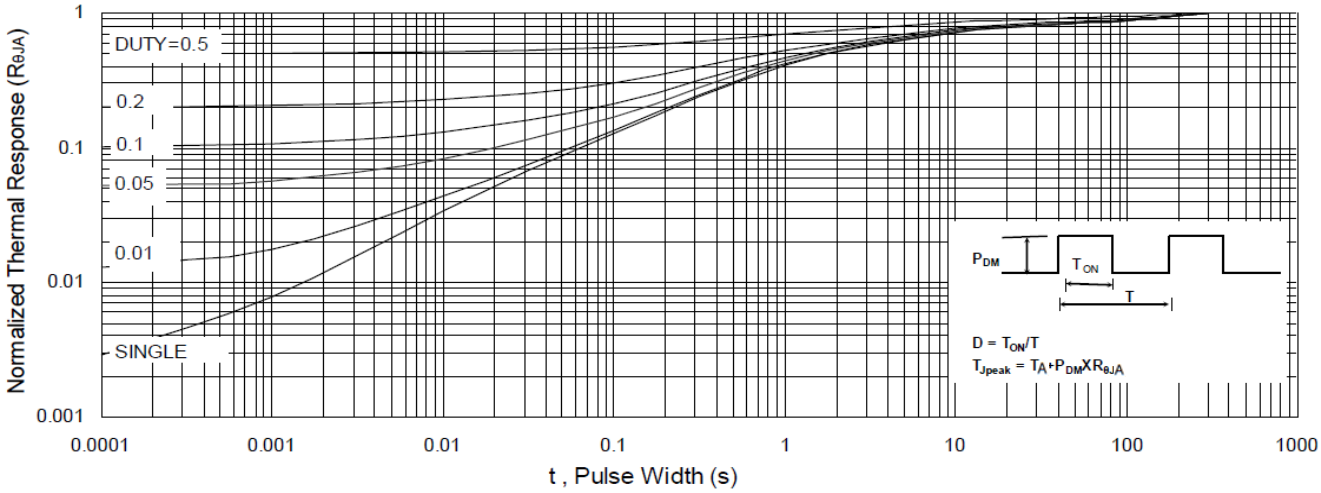


Fig.9 Normalized Maximum Transient Thermal Impedance

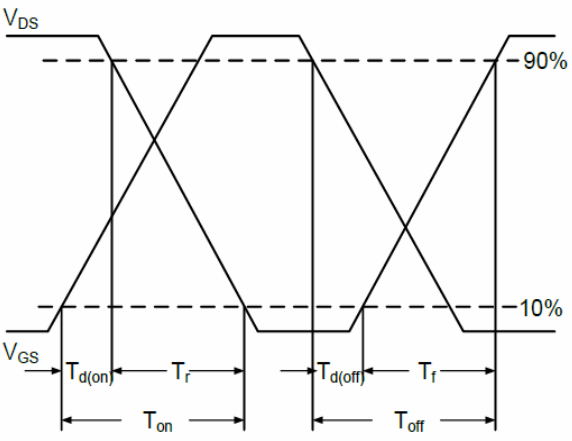


Fig.10 Switching Time Waveform

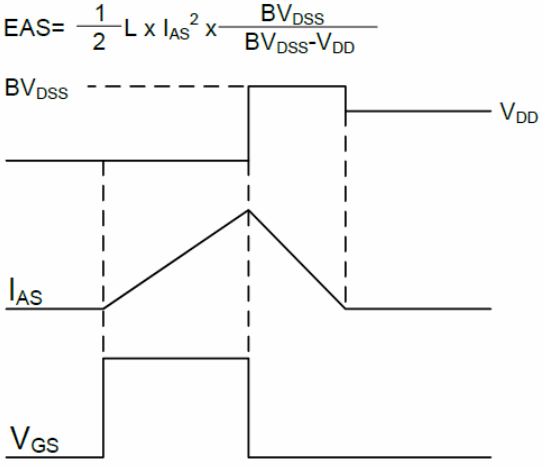


Fig.11 Unclamped Inductive Switching Waveform