

RoHS Compliant Product
A Suffix of "-C" specifies halogen & lead-free

FEATURES

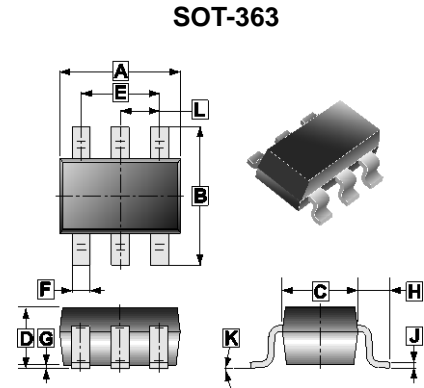
- Trench Power MV MOSFET Technology
- Voltage Controlled Small Signal Switch
- Low input Capacitance
- Fast Switching Speed
- Low Input / Output Leakage
- ESD Protected Up to 2kV (HBM)

MARKING

72KC

PACKAGE INFORMATION

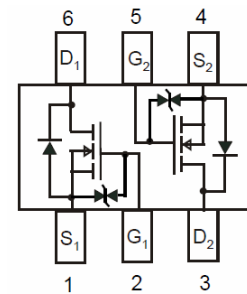
Package	MPQ	Leader Size
SOT-363	3K	7 inch



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	1.80	2.20	G	0.10	REF.
B	1.80	2.45	H	0.525	REF.
C	1.15	1.35	J	0.05	0.25
D	0.70	1.10	K	8°	
E	1.30 REF.		L	0.65 TYP.	
F	0.10	0.35			

ORDER INFORMATION

Part Number	Type
S2N72KCDW-C	Lead (Pb)-free and Halogen-free



MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DSS}	60	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current @V _{GS} =10V	I _D	T _A =25°C	300
		T _A =70°C	240
Pulsed Drain Current ¹	I _{DM}	1.5	A
Total Power Dissipation	P _D	300	mW
Operating Junction & Storage Temperature Range	T _J , T _{STG}	-55~150	°C
Thermal Data			
Thermal Resistance from Junction-Ambient ²	R _{θJA}	416	°C/W

ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	60	-	-	V	$V_{GS}=0, I_D=250\mu\text{A}$
Gate Threshold Voltage	$V_{GS(th)}$	1	-	2.5	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$V_{DS}=60\text{V}, V_{GS}=0$
Gate-Source Leakage	I_{GSS}	-	-	± 10	μA	$V_{DS}=0, V_{GS}=\pm 20\text{V}$
Static Drain-Source On-Resistance	$R_{DS(on)}$	-	1.9	2.5	Ω	$V_{GS}=10\text{V}, I_D=300\text{mA}$
		-	2	3		$V_{GS}=4.5\text{V}, I_D=200\text{mA}$
Total Gate Charge	Q_g	-	1.65	-	nC	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, I_D=0.3\text{A}$
Turn-On Delay Time	$T_{d(on)}$	-	6.5	-	nS	$I_D=300\text{mA}, V_{DD}=30\text{V}, V_{GS}=10\text{V}, R_{GEN}=6\Omega$
Turn-Off Delay Time	$T_{d(off)}$	-	9.6	-		
Input Capacitance	C_{iss}	-	27	-	pF	$V_{DS}=30\text{V}$ $V_{GS}=0$ $f=1\text{MHz}$
Output Capacitance	C_{oss}	-	3	-		
Reverse Transfer Capacitance	C_{rss}	-	2	-		
Source-Drain Diode						
Forward on Voltage	V_{SD}	-	-	1.2	V	$V_{GS}=0, I_S=300\text{mA}$
Continuous Source Current	I_S	-	-	300	mA	
Reverse Recovery Time	t_{rr}	-	24	-	nS	$V_{GS}=0, I_S=300\text{mA}, V_R=25\text{V}, di/dt=100\text{A}/\mu\text{s}$

Notes:

1. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
2. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

CHARACTERISTIC CURVES

Figure1. Output Characteristics

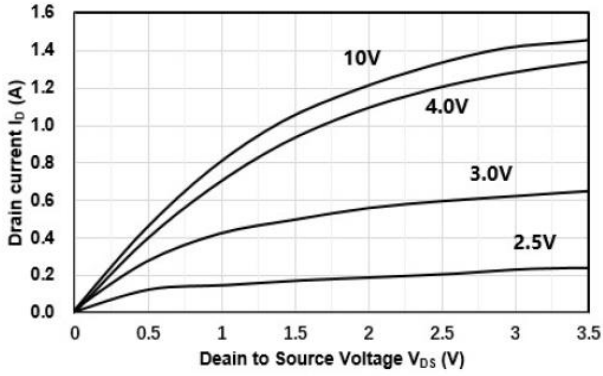


Figure2. Transfer Characteristics

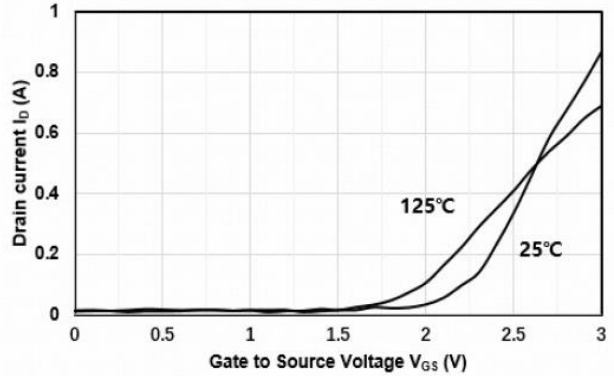


Figure3. Capacitance Characteristics

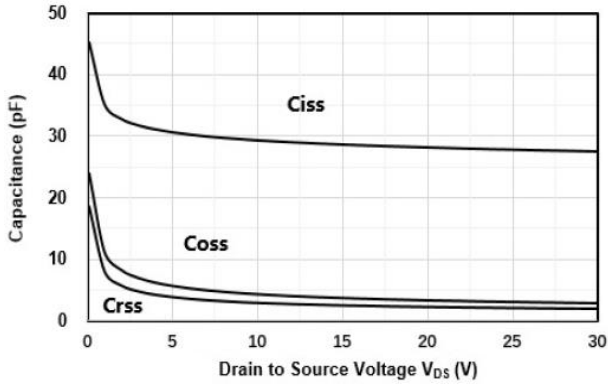


Figure4. Gate Charge

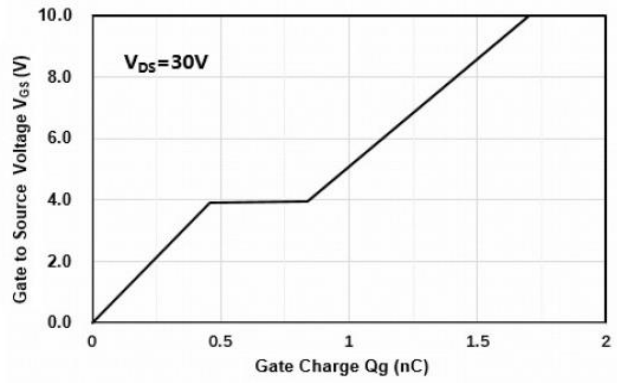


Figure5. Drain-Source on Resistance

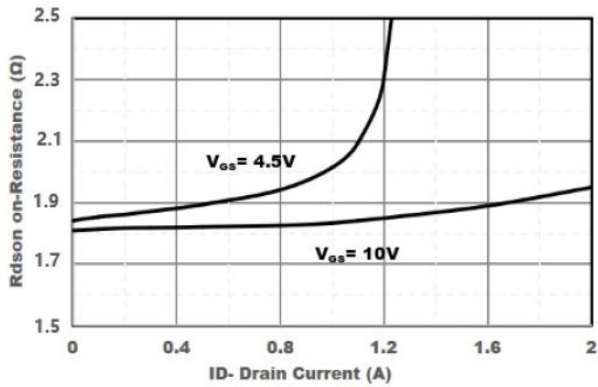


Figure6. Drain-Source on Resistance

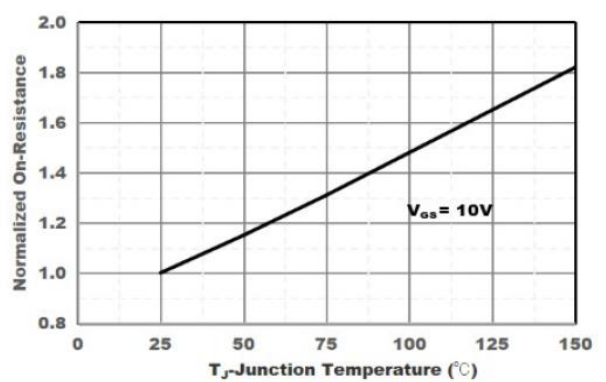


Figure7. Safe Operation Area

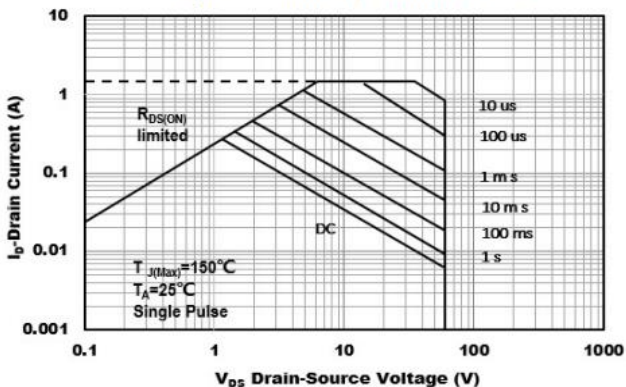


Figure8. Mounting Pad Layout

