Elektronische Bauelemente

SUM2P02-C

-0.86A, -20V, $R_{DS(ON)}$ 200mΩ

Dual P-Ch Enhancement Mode Power MOSFET

RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

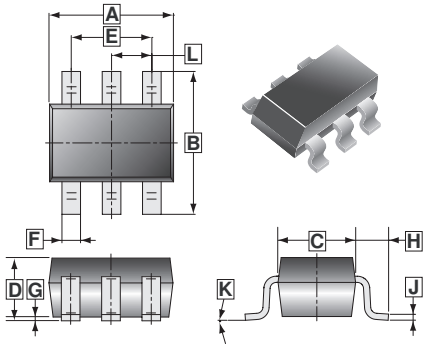
The SUM2P02-C is the highest performance trench Dual P-Ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The SUM2P02-C meet the RoHS and Green Product requirement with full function reliability approved.

FEATURES

- Advanced High Cell Density Trench Technology
- Super Low Gate Charge
- Green Device Available

SOT-363



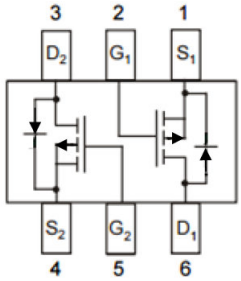
REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	1.80	2.20	G	0.100 REF.	
B	1.80	2.45	H	0.525 REF.	
C	1.15	1.35	J	0.08	0.25
D	0.80	1.10	K	8°	
E	1.10	1.50	L	0.650 TYP.	
F	0.10	0.35			

MARKING

2P02

PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-363	3K	7 inch



ORDER INFORMATION

Part Number	Type
SUM2P02-C	Lead (Pb)-free and Halogen-free

ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Ratings	Unit
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	±8	V
Continuous Drain Current [1], @V_{GS}= -4.5V	T_A=25°C	I_D	-0.86	A
	T_A=70°C		-0.68	
Pulsed Drain Current [3]		I_{DM}	-1.8	A
Total Power Dissipation	T_A=25°C	P_D	0.3	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Thermal Data				
Thermal Resistance Junction-Ambient [1]		$R_{\theta JA}$	417	°C/W
Thermal Resistance Junction-Ambient [2]			540	
Thermal Resistance Junction-Case [1]		$R_{\theta JC}$	305	

ELECTRICAL CHARACTERISTICS (T_J=25°C unless otherwise specified)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage		BV_{DSS}	-20	-	-	V	V_{GS}=0, I_D= -250uA
Gate Threshold Voltage		$V_{GS(th)}$	-0.5	-	-1	V	V_{DS}=V_{GS}, I_D= -250μA
Forward Transconductance		g_{fs}	-	3.4	-	S	V_{DS}= -5V, I_D= -0.86A
Gate-Source Leakage Current		I_{GSS}	-	-	±100	nA	V_{GS}= ±8V
Drain-Source Leakage Current	T_J=25°C	I_{DSS}	-	-	-1	μA	V_{DS}= -16V, V_{GS}=0
	T_J=55°C		-	-	-10		V_{DS}= -16V, V_{GS}=0
Static Drain-Source On-Resistance [4]		$R_{DS(ON)}$	-	170	200	mΩ	V_{GS}= -4.5V, I_D= -0.6A
			-	240	280		V_{GS}= -2.5V, I_D= -0.5A
Total Gate Charge		Q_g	-	4.6	-	nC	I_D= -0.86A V_{DS}= -20V V_{GS}= -4.5V
Gate-Source Charge		Q_{gs}	-	0.27	-		
Gate-Drain (“Miller”) Change		Q_{gd}	-	2.34	-		
Turn-on Delay Time		$T_{d(on)}$	-	11.6	-	nS	V_{DS}= -12V V_{GS}= -4.5V I_D= -0.86A R_G=3.3Ω
Rise Time		T_r	-	6.2	-		
Turn-off Delay Time		$T_{d(off)}$	-	31.8	-		
Fall Time		T_f	-	2.8	-		
Input Capacitance		C_{iss}	-	194	-	pF	V_{GS}=0 V_{DS}= -15V f=1MHz
Output Capacitance		C_{oss}	-	35.5	-		
Reverse Transfer Capacitance		C_{rss}	-	28.2	-		
Source-Drain Diode							
Continuous Source Current [1]		I_S	-	-	-0.6	A	
Pulsed Source Current [3]		I_{SM}	-	-	-1.2		
Forward On Voltage [4]		V_{SD}	-	-	-1.2	V	I_S= -0.6A, V_{GS}=0

Notes:
1. Surface Mounted on 1”x1” FR4 Board with 2OZ copper.
2. When mounted on Min. copper pad.
3. Pulse width limited by maximum junction temperature, Pulse Width≦300μs, Duty Cycle≦2%.
4. Pulse Test: Pulse Width≦300μs, Duty Cycle≦2%.

CHARACTERISTIC CURVES

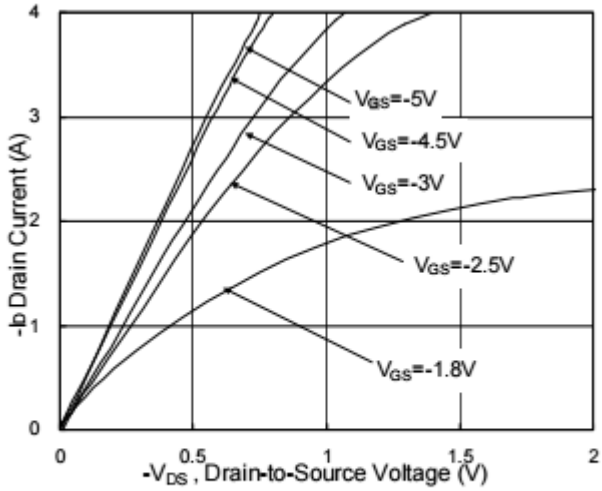


Fig.1 Typical Output Characteristics

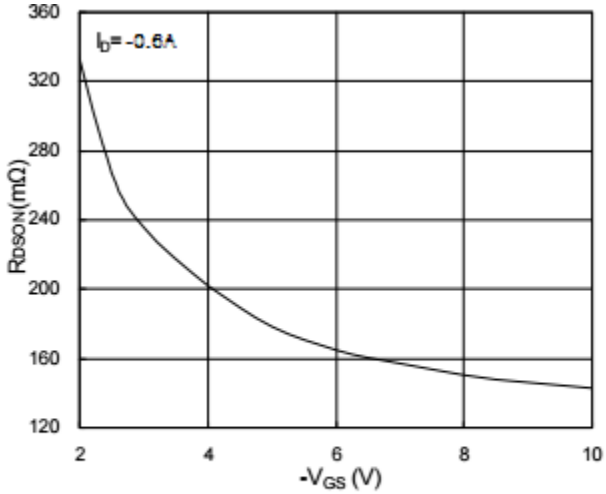


Fig.2 On-Resistance vs. Gate-Source

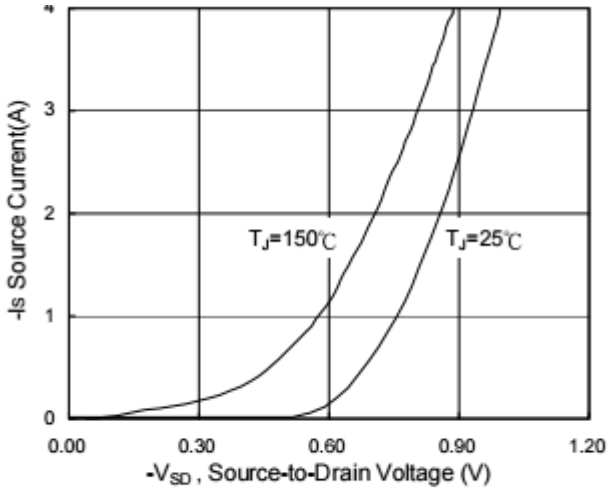


Fig.3 Forward Characteristics Of Reverse

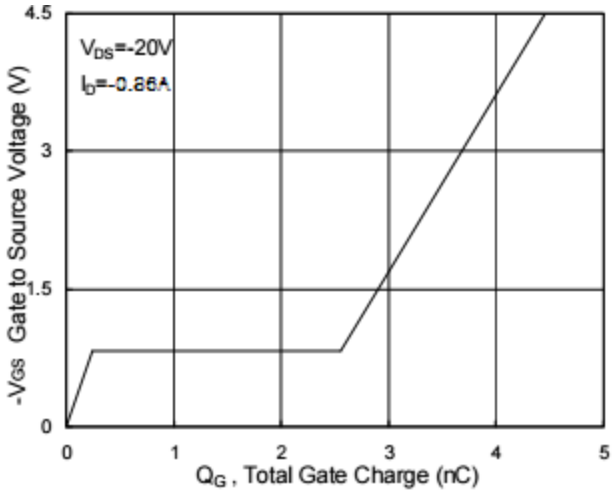


Fig.4 Gate-Charge Characteristics

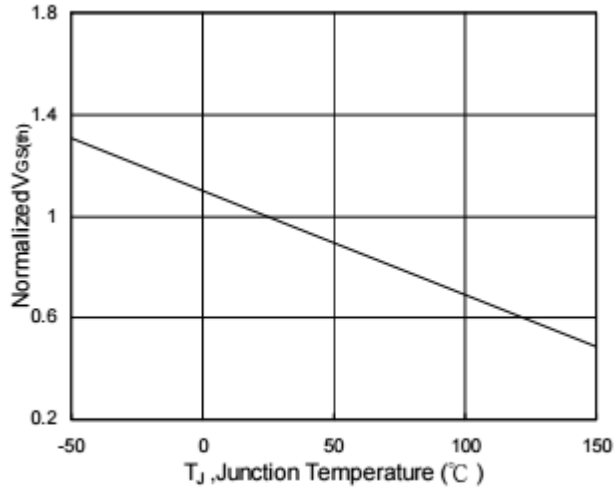


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

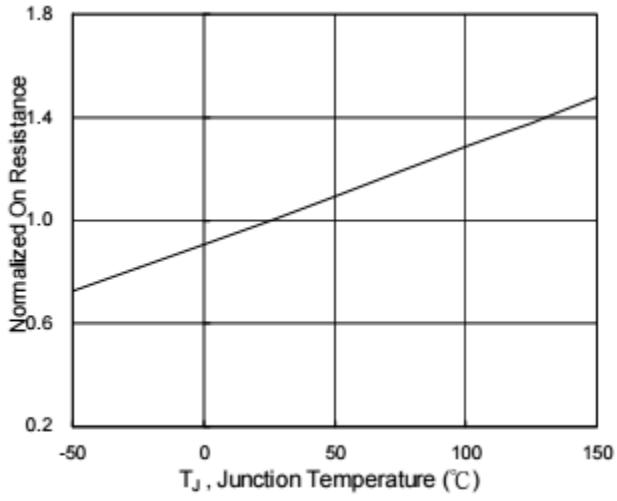


Fig.6 Normalized R_{DSON} vs. T_J

CHARACTERISTIC CURVES

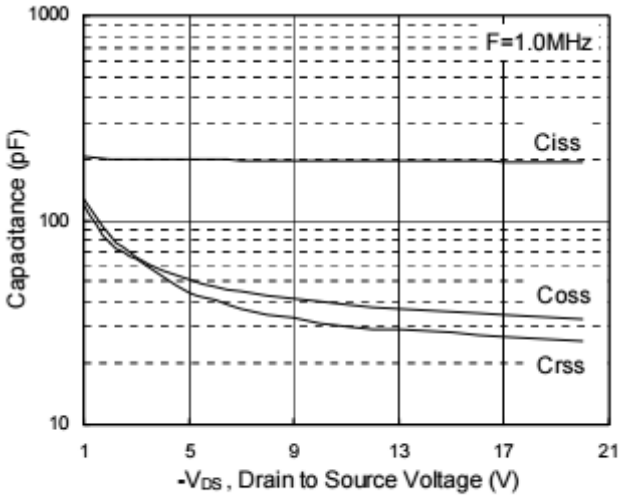


Fig.7 Capacitance

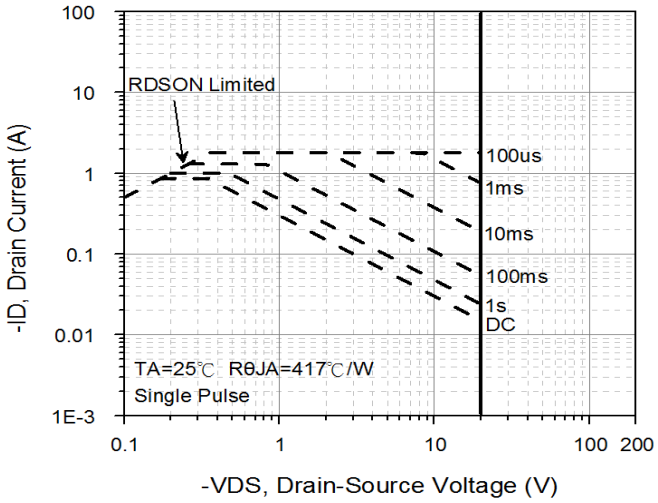


Fig.8 Safe Operating Area

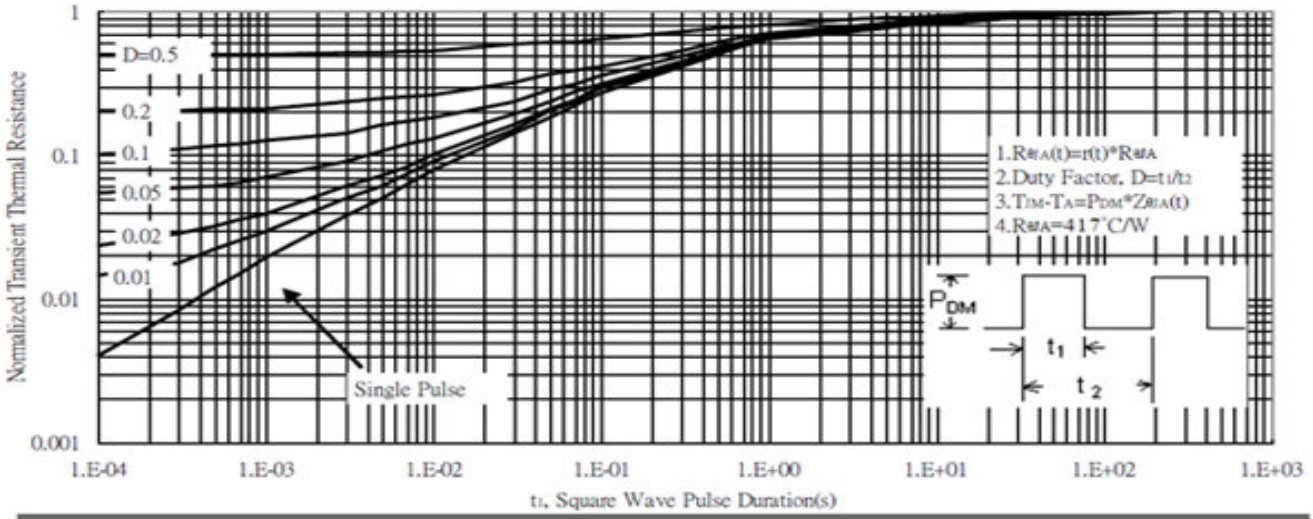


Fig.9 Normalized Maximum Transient Thermal Impedance

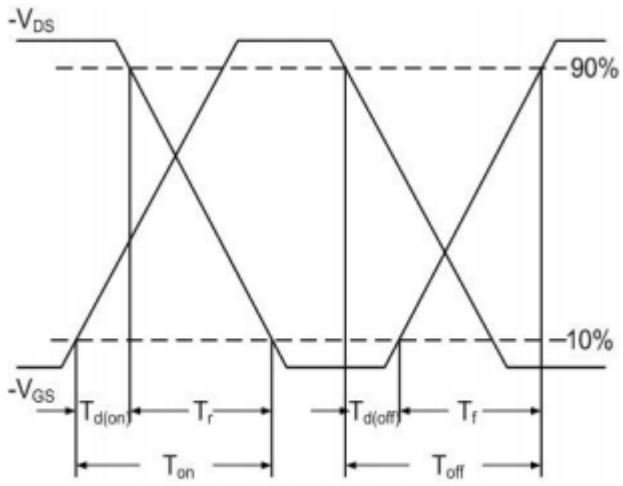


Fig.10 Switching Time Waveform

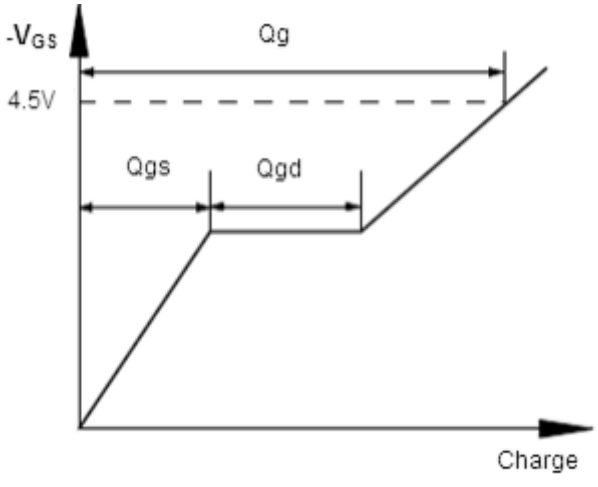


Fig.11 Gate Charge Waveform