

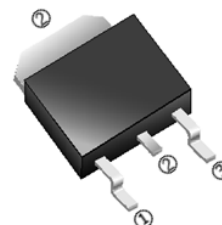
RoHS Compliant Product
A suffix of "-C" specifies halogen free

DESCRIPTION

These miniature surface mount MOSFETs utilize a high cell density trench process to provide Low $R_{DS(on)}$ and to ensure minimal power loss and heat dissipation.

Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

TO-252(D-Pack)



FEATURES

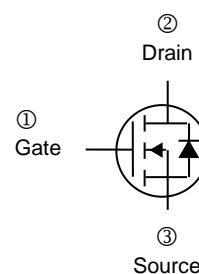
- Low $R_{DS(on)}$ provides higher efficiency and extends battery life.
- Low thermal impedance copper lead frame DPAK saves board space.
- Fast switching speed.
- High performance trench technology.

PACKAGE INFORMATION

Package	MPQ	Leader Size
TO-252	2.5K	13 inch

ORDER INFORMATION

Part Number	Type
SSD10N20-400D-C	Lead (Pb)-free and Halogen-free



ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	200	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	9.2	A
$T_C=25^\circ\text{C}$			
Pulsed Drain Current ²	I_{DM}	50	A
Continuous Source Current (Diode Conduction)	I_S	45	A
Total Power Dissipation	P_D	50	W
$T_C=25^\circ\text{C}$			
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55~175	$^\circ\text{C}$
Thermal Resistance Rating			
Maximum Thermal Resistance from Junction-Ambient ¹	$R_{\theta JA}$	50	$^\circ\text{C} / \text{W}$
Maximum Thermal Resistance from Junction-Case	$R_{\theta JC}$	3	$^\circ\text{C} / \text{W}$

Notes:

1. The surface of the device is mounted on a 1" x 1" FR4 Board.
2. The pulse width is limited by the maximum junction temperature.

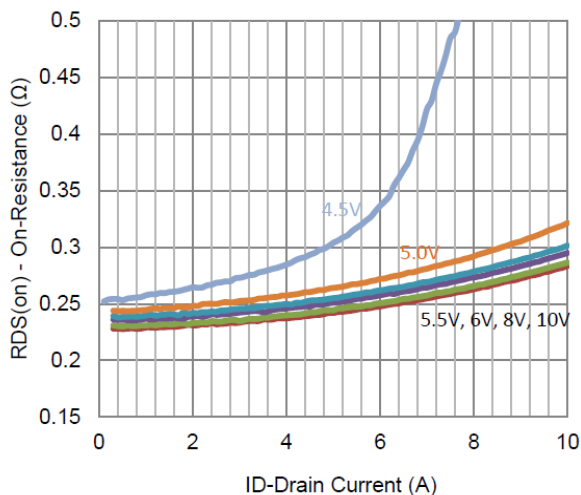
ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Gate-Threshold Voltage	V _{GS(th)}	1	-	3.5	V	V _{DS} =V _{GS} , I _D =250μA
Gate-Body Leakage Current	I _{GSS}	-	-	±10	μA	V _{DS} =0V, V _{GS} = ±20V
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μA	V _{DS} =160V, V _{GS} =0
		-	-	25		V _{DS} =160V, V _{GS} =0, T _J =55°C
On-State Drain Current ¹	I _{D(on)}	34	-	-	A	V _{DS} =5V, V _{GS} =10V
Drain-Source On-Resistance ¹	R _{DS(ON)}	-	-	400	mΩ	V _{GS} =10V, I _D =4A
		-	-	450		V _{GS} =5.5V, I _D =3.5A
Forward Transconductance ¹	g _{fs}	-	10	-	S	V _{DS} =15V, I _D =4A
Diode Forward Voltage	V _{SD}	-	0.95	-	V	I _S =23A, V _{GS} =0
Input Capacitance	C _{iSS}	-	409	-	pF	V _{GS} =0 V _{DS} =15V f =1MHz
Output Capacitance	C _{oSS}	-	59	-		
Reverse Transfer Capacitance	C _{rSS}	-	10	-		
Total Gate Charge	Q _g	-	6	-	nC	V _{DS} =100V V _{GS} =5.5V I _D =4A
Gate-Source Charge	Q _{gs}	-	2	-		
Gate-Drain Charge	Q _{gd}	-	3.3	-		
Turn-on Delay Time	T _{d(on)}	-	5.7	-	nS	V _{DD} =100V V _{GEN} =10V R _L =5Ω R _{GEN} =6Ω
Rise Time	T _r	-	29	-		
Turn-off Delay Time	T _{d(off)}	-	10.8	-		
Fall Time	T _f	-	35.8	-		

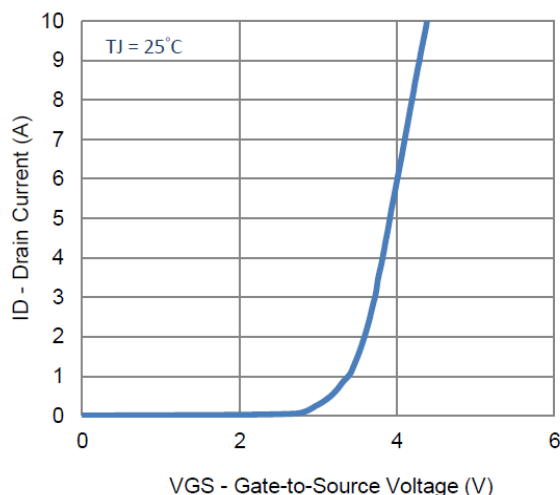
Note:

1. Pulse test : Pulse width ≤ 300μs, duty cycle ≤ 2%.

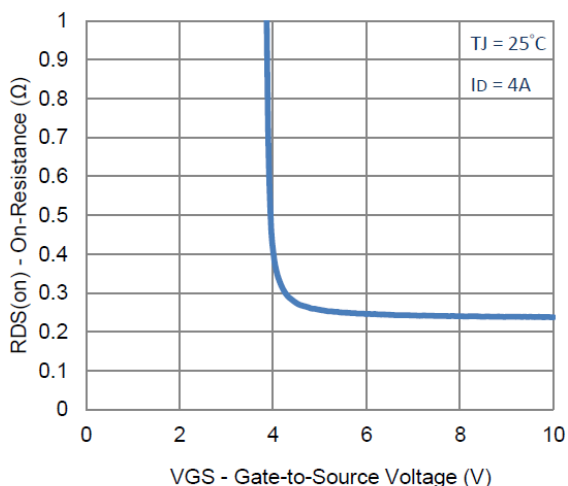
CHARACTERISTIC CURVE



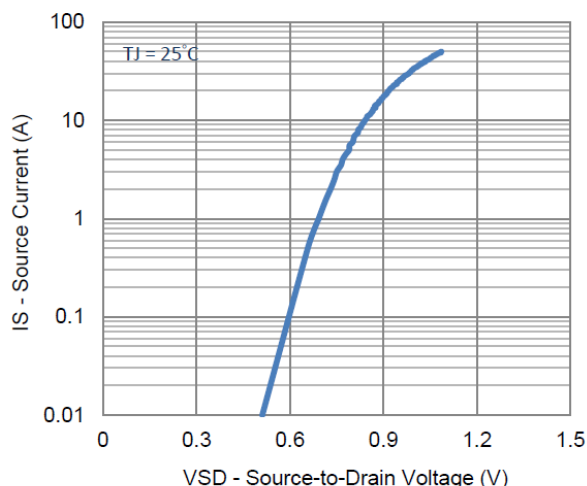
1. On-Resistance vs. Drain Current



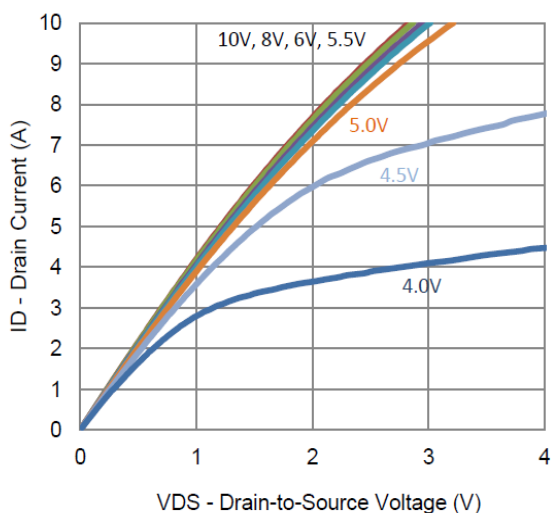
2. Transfer Characteristics



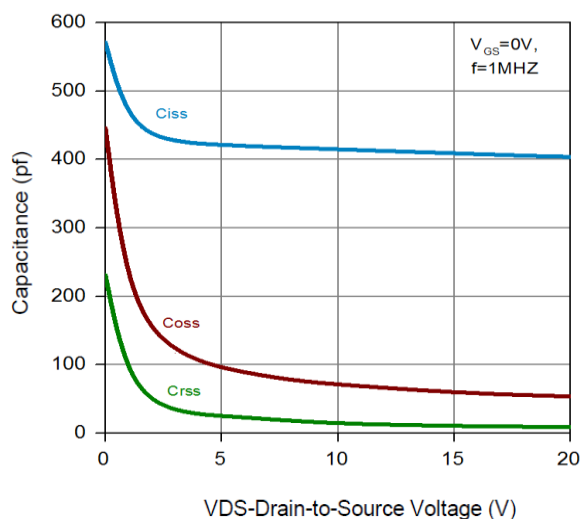
3. On-Resistance vs. Gate-to-Source Voltage



4. Drain-to-Source Forward Voltage

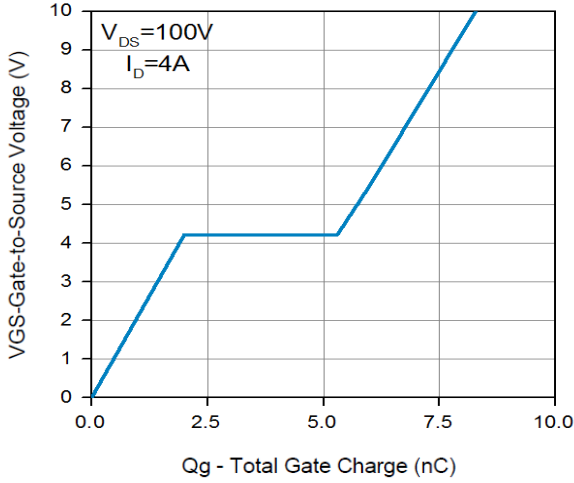


5. Output Characteristics

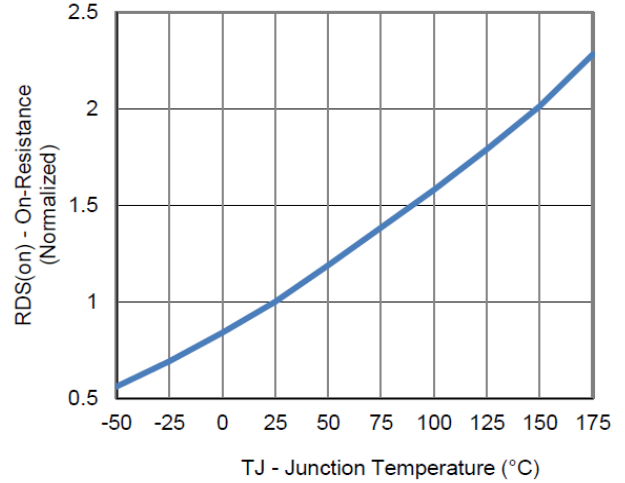


6. Capacitance

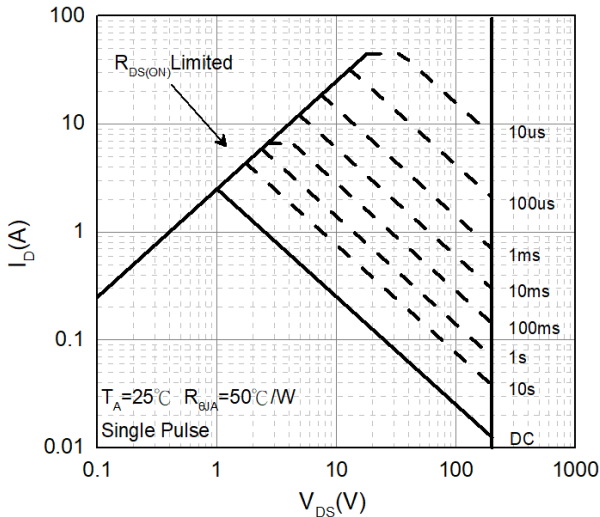
CHARACTERISTIC CURVE



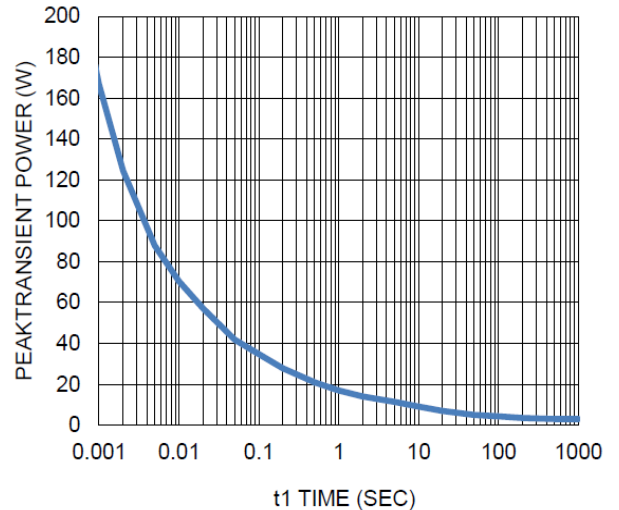
7. Gate Charge



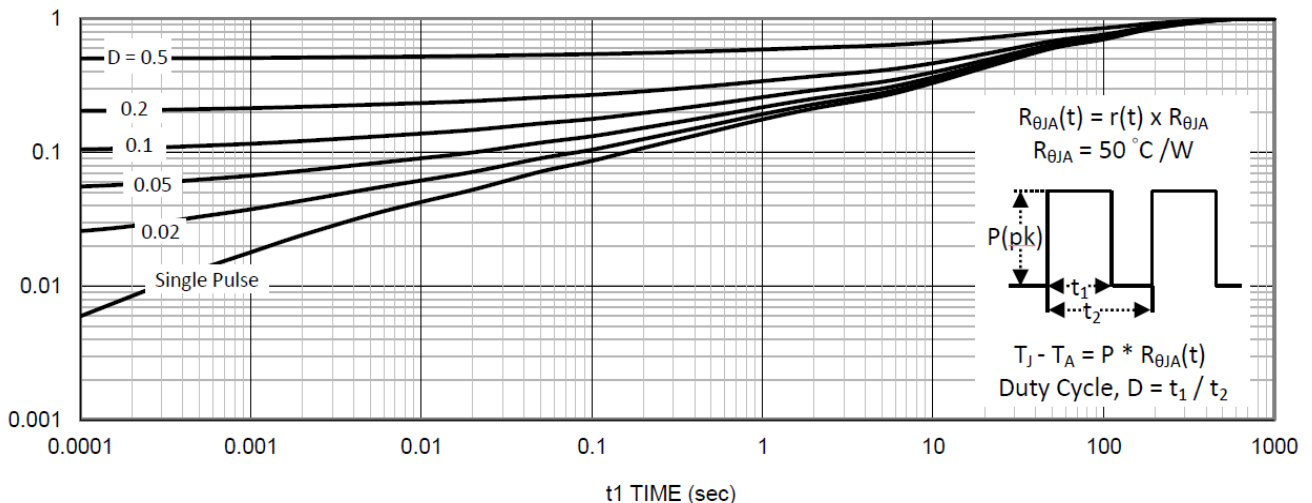
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area



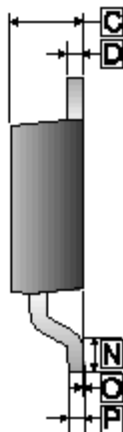
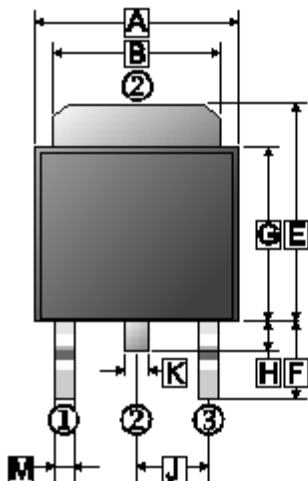
10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

PACKAGE OUTLINE DIMENSIONS

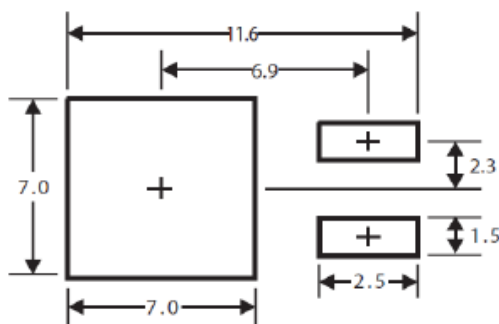
TO-252



REF.	Millimeter	
	Min.	Max.
A	6.30	6.90
B	4.95	5.53
C	2.10	2.50
D	0.40	0.90
E	6.00	7.70
F	2.90 REF.	
G	5.40	6.40
H	0.60	1.20
J	2.30 REF.	
K	0.89 REF.	
M	0.45	1.14
N	1.55 TYP.	
O	0	0.15
P	0.58 REF.	

MOUNTING PAD LAYOUT

TO-252



*Dimensions in millimeters